

Koneru Lakshmaiah Education Foundation

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Workshop on ASIC design and SPLD design

Date: 06/09/2023

Department Name	: ECE			
Event Date(s)	: 06/09/2023			
Event Time	: 03.00 pm to 05.00 pm.			
Event Name	: Design and applications of CPLD and FPGA			
Event Category	: Department Association Activity-IEEE EDS			
Event Objective	: To design and development of CPLD and FPGA			
Event Venue / Room No.	: R106			
Faculty Coordinator EMP.ID	: 5155			
Faculty Coordinator Name	: Dr.B.Balaji, Associate Professor, ECE			
Faculty Coordinator Email-I	D: <u>balaji@kluniversity.in</u>			
Student Organizers	: M.Koushik			
The following KLEF students	s have attended			
No. of Volunteers Participate	d: 09			
No. of faculty Participated: 02				

S.No	Reg.No	Name of the Student	Signature
1	2100049029	LAVANYA MACHUNOORI	MACHUNDORI
2	2100049101	SINGAMREDDY VISHNU VARDHAN REDDY	die
3	2100049029	LAVANYA MACHUNOORI	Laveny
4	2100049101	SINGAMREDDY VISHNU VARDHAN REDDY	VAR Duan Bad
5	2100049024	THALLAM VENKATA NAGA SAI MANASWINI	Have
6	2100049026	JANGA MANOJ KUMAR REDDY	Kymog peddy

7	2100049037	PAMARTHI RAVI TEJA	1765 A
8	2100049019	KASARAM AARSHA	AARSA
9	2100040244	SANJAMALA SRI VIDYA	SSIN



Event Outcome:

The Students have learnt the basic design of CPLD and FPGA under IEEE EDS Program for its specific application converge towards advancing semiconductor technology, improving device performance, enhancing reliability, and driving innovation in various applications ranging from consumer electronics to healthcare and automotive systems.

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Dr.B.Balaji Signature of Faculty Coordinator

Dr.M.Supan, Head of the Department. DECE NL University Professor & Head Department of ECE K L E F Green Fields, Vaddeswaran.

