



## Koneru Lakshmaiah Education Foundation

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.

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Admin Off: 29-35-38, Museum Road, Gopurpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Ref: KLEF/RO/ECE/CIRCULAR

Date:01-03-2019

### CIRCULAR

**Sub:** Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.

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This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Workshop on Exploration of advanced boards in FPGA design" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 04-03-2019, as details below:

Event Name: "Workshop"


Date: 04-03-2019

Venue: R-106

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

To  
All ECE Students,  
All ECE Faculty,  
Principal.

  
**HOD-ECE**  
Professor & In-charge HOD  
Department of ECE  
K L University  
VADDESWARAM  
Guntur Dt., A.P., India.



## **Koneru Lakshmaiah Education Foundation**

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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### **A One-Day Workshop On “Exploration of advanced boards in FPGA design”**

**By**

**Always@VLSI**

**Department Of ECE**

**Name of the event:** Exploration of advanced boards in FPGA design

**Dates:** 04-03-2019

**Venue:** R106

**No. of students participated:** 65

#### **Objective of the event:**

The event organized by Always@vlsi Technical Club titled "Exploration of Advanced Boards in FPGA Design" aims to provide participants with an in-depth understanding of cutting-edge FPGA (Field-Programmable Gate Array) technology. Through this event, attendees will delve into the intricacies of FPGA design, focusing on advanced boards that facilitate innovation and development in various fields such as telecommunications, automotive, aerospace, and beyond. Participants can expect to explore the latest advancements in FPGA hardware, software tools, and design methodologies, equipping them with valuable insights to tackle complex design challenges and unleash the full potential of FPGA technology. Through hands-on workshops, expert lectures, and interactive sessions, this event promises to be a platform for enthusiasts and professionals alike to expand their knowledge, network with peers, and stay abreast of the ever-evolving landscape of FPGA design.

- Day-1: Introduction to FPGA design
- Day-2: Digital design through verilog
- Day-3: Hands on experience using Basys-3 Boards
- Day-4: Hands on experience using ZYNQ Boards

**Description of the event:**

Always@vlsi Technical Club's event, "Exploration of Advanced Boards in FPGA Design," offers participants a comprehensive dive into the realm of FPGA (Field-Programmable Gate Array) technology. Through a series of workshops, seminars, and hands-on sessions, attendees will have the opportunity to explore the latest advancements in FPGA hardware and design methodologies. From understanding the architecture of advanced FPGA boards to mastering cutting-edge software tools, participants will gain invaluable insights into leveraging FPGA technology across various industries, including telecommunications, automotive, and aerospace. With expert-led discussions and practical demonstrations, this event serves as a platform for enthusiasts and professionals to expand their knowledge, exchange ideas, and stay ahead in the dynamic field of FPGA design.

**Outcome of the event:**

The event "Exploration of Advanced Boards in FPGA Design," yielded significant outcomes for participants. Attendees gained a deeper understanding of advanced FPGA technologies, including their architecture, capabilities, and applications across diverse industries. Through hands-on workshops and expert-led sessions, participants honed their skills in FPGA design and learned to leverage cutting-edge hardware and software tools effectively. Moreover, networking opportunities allowed attendees to connect with peers and industry experts, fostering collaborations and knowledge exchange. Overall, the event empowered participants to tackle complex design challenges and innovate in their respective fields, thereby contributing to the advancement of FPGA technology and its broader impact on various sectors.

**Photos of the event:**



**Students participating in FPGA design flow discussion and demonstration.**

**Participant's List:**

S.NO	ID.NO	NAME	BRANCH	SIGNATURE
1.	160041058	KANNEGANTI PAVAN	ECE	Pavan
2.	160041035	JADDU JAYASAI KISHORE	ECE	J. Kishore
3.	160041022	GORRIPATI DIVYA SAI TEJA	ECE	Teja
4.	160041019	KIRIGADALA RUDREGOWDA	ECE	Rudregowda
5.	160041017	VINJAMURI PAVAN KUMAR	ECE	Kumar
6.	160041010	KOLLIPARA SAI SREE ROHINI	ECE	Rohini
7.	160041008	KANKANAMPATI MANISHA	ECE	Manisha
8.	160041006	JANGA SATYA RISHI TEJA	ECE	J. Teja
9.	160040996	KONAKALLA GEETHA SREE	ECE	Geetha
10.	160040989	YEDDULA JITENDRA REDDY	ECE	Jitendra Reddy
11.	160040976	YADAVALLI SUNDAR SAI	ECE	Sai
12.	160040975	YACHAMANENI TANMAYEE	ECE	Tanmayee
13.	160040963	VENKATA SAI AJAY	ECE	Ajay
14.	160040948	VELLATURI VENKATA	ECE	Venkata
15.	160040947	VENKATA PRUDHVI	ECE	V. Prudhvi
16.	160040939	VEERA LOHIT K	ECE	K. Lohit
17.	160040925	SIVA PRUDHVISH VALIVETI	ECE	Valiveti
18.	160040922	VALE TARUN KUMAR	ECE	Kumar
19.	160040921	VAKALAPUDI SAI RAJESH	ECE	Sai Rajesh
20.	160040920	VADLAMUDI VENKATA	ECE	Venkata
21.	160040916	VADDEVALLI PAVAN	ECE	Pavan
22.	160040910	KOTI REDDY UPPELA	ECE	Uppeela
23.	160040909	UPPALAPATI CHANDU	ECE	Chandru
24.	160040904	S. TUMPUDI NIKHILESH	ECE	S. Nikhilesh
25.	160040895	P. THUMMURU	ECE	P. Thummuru
26.	160040894	B. PRASANTH KUMAR REDDY	ECE	B. Prasanth
27.	160040891	MADHUSUDHAN	ECE	M. Adshu
28.	160040890	ABHISHEK THOTAKURA	ECE	Abhishek
29.	160040885	THOPURI CHANAKYA	ECE	T. Chanakya
30.	160040884	THOMMANDRU JAYARAM	ECE	T. Jayaram
31.	160040883	THIRUMALASETTY BALAJI	ECE	Balaji
32.	160040859	SURI KAVYA	ECE	S. Kavya
33.	160040858	NARENDRA SURARAPU	ECE	N. Teja
34.	160040854	SUNKAVALLI SURYA TEJA	ECE	S. Teja
35.	160040850	M. SUDINEEDI VENKATA	ECE	Venkata
36.	160040841	SOMU VENKATASAINIKHIL	ECE	S. Venkata
37.	160040840	SOMIREDDY JAYA SAI SRI	ECE	J. Jayasri
38.	160040830	SHAKAMURI MEGHANA	ECE	M. Meghana
39.	160040829	SHAIK YASWANTH BASHA	ECE	Y. Basha
40.	160041058	KANNEGANTI PAVAN	ECE	K. Pavan
41.	160041035	JADDU JAYASAI KISHORE	ECE	J. Kishore
42.	160041022	GORRIPATI DIVYA SAI TEJA	ECE	G. Teja
43.	160041019	KIRIGADALA RUDREGOWDA	ECE	Rudregowda
44.	160041017	VINJAMURI PAVAN KUMAR	ECE	P. Kumar
45.	160041010	KOLLIPARA SAI SREE ROHINI	ECE	Sree Rohini

46.	160041008	KANKANAMPATI MANISHA	ECE	Manisha
47.	160041006	JANGA SATYA RISHI TEJA	ECE	
48.	160040996	KONAKALLA GEETHA SREE	ECE	K. Geetha Sree
49.	160040989	YEDDULA JITENDRA REDDY	ECE	Jitendra V
50.	160040976	YADAVALLI SUNDAR SAI	ECE	sundasa V
51.	160040975	YACHAMANENI TANMAYEE	ECE	Tanmayee V
52.	160040963	VENKATA SAI AJAY	ECE	Sai Ajay
53.	160040948	VELLATURI VENKATA	ECE	venkata V
54.	160040947	VENKATA PRUDHVI	ECE	V. Prudhvi
55.	160040939	VEERA LOHIT K	ECE	Veera Lohit
56.	160040925	SIVA PRUDHVISH VALIVETI	ECE	Prudhvi
57.	160040922	VALE TARUN KUMAR	ECE	Tarun
58.	160040921	VAKALAPUDI SAI RAJESH	ECE	Sai Rajesh
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64.	160040895	THUMMURU	ECE	Thummuru
65.	160040894	PRASANTH KUMAR REDDY	ECE	P. Kumar Reddy

S. Vairam

In charge

Always@VLSI Technical Club

S. Vairam

HOD-ECE

Professor & Alternate HOD  
Department of E.

K L University

VADESWARAM

Guntur Dt., A.P.,