



Koneru Lakshmalah Education Foundation
(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.
Phone No. 08645 - 350200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in
Admin Off: 29-35-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 888 - 3500122, 2576129.

Ref: KLEF/RO/ECE/CIRCULAR

Date: 03-07-2020

CIRCULAR

Sub: Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.


This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Workshop on Analog Circuit Design And Simulation" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 06-07-2020, as details below:

Event Name: "Workshop"
Date: 06-07-2020
Venue: R-106

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

To
All ECE Students,
All ECE Faculty,
Principal.


HOD-ECE
Professor & Alternate HOD
Department of E
K L University
VADESARAM
Guntur Dt., A.P.,



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A Three-Day Workshop On “Analog Circuit Design And Simulation”

By

Always@VLSI

Department Of ECE

Name of the event: Analog Circuit Design And Simulation

Dates: 06-07-2020

Venue: R106

No. of students participated: 71

Objective of the event:

The "Analog Circuit Design And Simulation" event organized by the Always@VLSI technical club aims to provide participants with a comprehensive understanding of analog circuit design principles and simulation techniques. Through a series of workshops, seminars, and practical demonstrations, the event seeks to equip attendees with the knowledge and skills necessary to design, simulate, and analyze analog circuits effectively. Participants will have the opportunity to explore various tools and software platforms commonly used in the industry, gaining hands-on experience in circuit simulation and validation. Additionally, the event aims to foster a collaborative learning environment where participants can engage with experts, share insights, and collaborate on innovative circuit design projects. Ultimately, the objective is to empower attendees to become proficient analog circuit designers capable of tackling real-world challenges in the field of VLSI technology.

Description of the event:

The "Analog Circuit Design And Simulation" event, meticulously organized by the Always@VLSI technical club, is a comprehensive platform tailored for enthusiasts and professionals alike, delving into the intricate world of analog circuitry. Through a blend of informative seminars, hands-on workshops, and interactive demonstrations, participants are

immersed in the art and science of designing and simulating analog circuits. Led by seasoned industry experts, attendees explore fundamental principles, advanced techniques, and emerging trends in analog circuit design. Leveraging state-of-the-art simulation tools, participants gain practical experience in modeling, analyzing, and optimizing various circuit configurations. Moreover, the event fosters a collaborative environment, encouraging networking, knowledge sharing, and fostering lasting connections within the VLSI community. With a focus on practical learning and skill enhancement, this event equips participants with the necessary expertise to tackle real-world challenges and excel in the dynamic field of analog circuit design.

Outcome of the event:

The "Analog Circuit Design And Simulation" event resulted in substantial outcomes for participants, ranging from enriched knowledge to enhanced practical skills. Attendees gained a deep understanding of analog circuit design principles and simulation methodologies through engaging workshops and informative sessions led by industry experts. By leveraging state-of-the-art simulation tools, participants were able to develop and validate complex analog circuits, honing their abilities in modeling and analysis. Furthermore, the event provided valuable networking opportunities, allowing participants to connect with peers and professionals, fostering collaboration and knowledge exchange within the VLSI community. Overall, the event facilitated the personal and professional growth of participants, equipping them with the expertise and connections necessary to excel in the dynamic field of analog circuit design and simulation.

Photos of the event:



Constructing schematic, symbols and simulating the analog circuits

Participant's List:

S.NO	STUDENT ID	STUDENT NAME	DEPT	SIGNATURE
1.	170041066	CHIMALAMARRI BHANU	ECE	Bhanu
2.	170041064	FOUZIA SYED	ECE	SYED
3.	170041063	VANUKURI MEHER THANMAIEE	ECE	Thanmai
4.	170041058	SAKHAMURI MANICHANDU	ECE	manichand
5.	170041057	NALLAMOTHU LOHITH KUMAR	ECE	Lohith
6.	170041054	MADDINENI GREESHMA	ECE	Greeshma
7.	170041046	MAKKE VENKATA NARASIMHA	ECE	Narasimha
8.	170041042	ELURI DHATHRI	ECE	Dhathri
9.	170041040	PARASU SAI PRASANTHI	ECE	Prasanthi
10.	170041039	LAKKAKULA SAI SUMA	ECE	Suma
11.	170041037	UPASI VENKATA KRISHNA	ECE	Venkata
12.	170041036	RAVURI ESWAR TEJA	ECE	Eswar
13.	170041035	PULLAMSETTI SUMAN	ECE	suman
14.	170041032	MANEPALLI SATYA SAI VITHAL	ECE	Sai vithal
15.	170041029	NIKHIL TEJA K	ECE	Nikhil Teja
16.	170041027	BANDARU AVINASH	ECE	Avinash
17.	170041024	VEERANKI JYOTHIRMAI	ECE	Jyothi
18.	170041023	THORLIKONDA GOPI	ECE	Gopi
19.	170041019	RAJABOINA MANOJ MAHINISH	ECE	Manoj
20.	170041014	KOLLA NARASIMHA	ECE	Narasimha
21.	170041013	KOLA RAMYA SRI VENI	ECE	Ramyasri
22.	170041012	KANDRU BHARATH	ECE	Bharath
23.	170041010	IDARAPALLI SAI SOURAV	ECE	Sai Sourav
24.	170041005	YERUVA VENKATA SAI KRISHNA	ECE	Sai Krishna
25.	170040986	MADHU YELUBANDY	ECE	Madhu
26.	170040980	Y NagaSai Neeraja	ECE	Neeraja
27.	170040977	YANNAM PAVAN TEJA	ECE	Pavan
28.	170040972	Lakshmi Iyava deepika Yallapu	ECE	Deepika
29.	170040970	SUNDEEP YALAMANCHILI	ECE	Sundeep
30.	170040968	YAKKALURI GOUTHAM	ECE	Goutham
31.	170040966	VASANTHI YADLAPALLI	ECE	Yadlapalli
32.	170040964	YADLA HESHMA CHOWDARY	ECE	Heshma
33.	170040962	YADAVALLI SAI SREE VISHNU	ECE	Saisree
34.	170040960	YADALAPALLI SRI SAI SUMANTH	ECE	Sai Sumanth
35.	170040956	VUMMDI SETTY MONIKA	ECE	Monika
36.	170040946	VIPPADAPU HARSHAVARDHAN	ECE	Harshavardhan
37.	170040944	VIKKURTY JEEVAN SAI	ECE	Jeevan
38.	170040942	DILEEP TEJA VERRIBOINA	ECE	Dileep
39.	170040935	SAI PRAKASH VEMURI	ECE	Prakash
40.	170040933	VEMULA BHASKAR RAM	ECE	Bhaskar
41.	170040930	VELUPULA NAGI REDDY	ECE	Nagi
42.	170040928	CHINNA CHAKRA RAO VELNATI	ECE	Chinna
43.	170040926	VELLANKI DIVYA	ECE	Divya

S.NO	STUDENT ID	STUDENT NAME	DEPT	SIGNATURE
44.	170040925	VELICHETI VENKATA SAI	ECE	Sai
45.	170040924	VELAMAKANNI VENKATA NAGA	ECE	Venkat
46.	170040920	VEERAVALLI SWETHA	ECE	Swetha
47.	170040914	RACHANA VARANASI	ECE	Rachana
48.	170040912	VANIPALLY MANISH REDDY	ECE	Manish
49.	170040909	VALLI SRINISHNA BHATTIPROLU	ECE	Srinishna
50.	170041066	CHIMALAMARRI BHANU	ECE	Bhanu
51.	170041064	FOUZIA SYED	ECE	Fuzia
52.	170041063	VANUKURI MEHER THANMAIEE	ECE	Meher
53.	170041058	SAKHAMURI MANICHANDU	ECE	Manichandu
54.	170041057	NALLAMOTHU LOHITH KUMAR	ECE	Lohith
55.	170041054	MADDINENI GREESHMA	ECE	Greeshma
56.	170041046	MAKKE VENKATA NARASIMHA	ECE	Narasimha
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58.	170041040	PARASU SAI PRASANTHI	ECE	Prasanthi
59.	170041039	LAKKAKULA SAI SUMA	ECE	Suma
60.	170041037	UPASI VENKATA KRISHNA	ECE	Krishna
61.	170041036	RAVURI ESWAR TEJA	ECE	Eswar
62.	170041035	PULLAMSETTI SUMAN	ECE	Suman
63.	170041032	MANEPALLI SATYA SAI VITHAL	ECE	Satyasai
64.	170041029	NIKHIL TEJA K	ECE	Teja
65.	170041027	BANDARU AVINASH	ECE	Avinash
66.	170040908	VALLEPU MANI KRISHNA	ECE	Mani
67.	170040907	VALLALA TEJA SAI PRABHU	ECE	Teja Sai
68.	170040905	VAKKANTI VISHNU PRIYA	ECE	Priya
69.	170040898	VAMSI KRISHNA UYYURI	ECE	Vamsi Krishna
70.	170040895	UPPALAPATI VINAY KUMAR	ECE	Vinay
71.	170040893	UPPALAPATI AKSHI THA	ECE	Akshi

S. Vait

In charge

Always@VLSI Technical Club

S. Suresh

HOD-ECE

Professor & Alternate HOD
Department of E.

K L University

VADESWARAM
Guntur Dt., A.P.