



REPORT

Recent Trends in VLSI Chip/ Devices for Sensor Applications

Date: 08-11-2022, koneru lakshmaiah education foundation (deemed to be university)

ATAL FDP Program on Recent Trends in VLSI Chip/Devices for Sensor Applications

Recent Trends in VLSI Chip/ Devices for Sensor Applications

VLSI chip/ Devices for sensor applications, AICTE sponsored FDP was conducted from 26th September to 1st October 2022 in Online mode, 26th October to 1st November 2022 in offline in which around 50 participants from different parts of the country participated.

The speakers for the Two week FDP “Recent Advancements in VLSI Chip/ Devices for Sensor Applications” were:

ON-Line Speakers

Dr. Koushik Guha Assoc. Prof, NIT Silchar



Education

- **Ph.D.:** National Institute of Technology Silchar, Assam, India, 2016
- **M.Tech.:** Burdwan University, West Bengal, India, 2007
- **B.Tech.:** West Bengal University of Technology, Kolkata, India, 2005

Experience

- **06 July 2022 to present: Associate Professor**, National Institute of Technology Silchar
- **01 Jun 2018 to 06 July 2022: Assistant Professor Grade-I**, National Institute of Technology Silchar.
- **Jun 2010 to 31 May 2018: Assistant Professor Grade-II**, National Institute of Technology Silchar.
- **27 Aug 2007 to 14 Jun 2010: Lecturer**, Haldia Institute of Technology, Department of Electronics and Communication Engineering, W. B, India.
- **2005 - 2007 - Software Engineer**, IBM India Pvt Ltd

Dr Balwinder Raj, Assoc. Prof NITTTR Chandigarh.



Education

Postdoc-2011 VLSI Design (University of Rome, Italy)
Ph.D-2010 Microelectronics and VLSI Design (IIT Roorkee)
M.Tech-2006 Microelectronics (Panjab University Chandigarh)
B.Tech-2004 Electronics Engineering (Punjab Technical University)

Experience

2019 – Present: **Associate Professor.ECE,NIT ,Chandigarh**
2012 – 2019: **Assistant Professor ECE**
Dr B R Ambedkar National Institute of Technology, Jalandhar
2011 – 2012: **Assistant Professor**
ABV-Indian Institute of Information Technology and Management, Gwalior

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Dr.K.Srinivas Rao, KL University



Education

- **Ph.D.** Central University, 2009
- **M.Tech** BITS Pilani, 2006

Experience

- April 2015 - present: **Professor**, EceDept KLEF
- February 2010 - March 2015 :Professor & HOD,EIE **Lakireddy Bali Reddy College of Engineering.**
- July 2006 - June 2009: **Assistant Professor** Assam University.

Dr.Suman lata Tripathi.professor lovely professional university.punjab.



Education

- **Ph.D.** in the area of microelectronics and VLSI from MNNIT, Allahabad. 2015
- **M.Tech** UP Technical University, 2010

Experience

- Jan 2019 - present: **Professor lovely professional university.punjab**
- Jan 2017 - Jan 2019 : **Assistant Professor , lovely professional university.punjab**

Dr. Y.Naresh, Malla Reddy University.



Education


TECH: SV University 2014
M.TECH:JNTUK 2016
PHD:KLEF 2018

Experience

Jan 2017 - present: : **Assistant Professor , Malla Reddy University**

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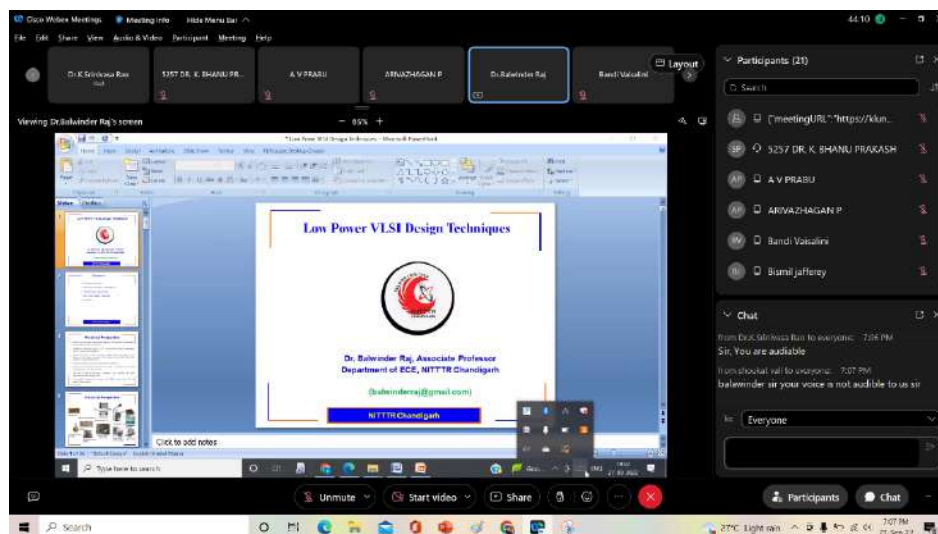
Dr.Keethi Assoc Professor, JNTUH

	EDUCATION
	B.Tech: - Electronics and Communication Engineering, Malla Reddy College of Engineering For Women-Suraram, Hyderabad 2014.
	M.Tech: - VLSI System Design, BVRaju Institute of Technology, Narsapur 2016
	EXPERIENCE
	2017-present: Dr.Keethi Assoc Professor, JNTUH

Online Schedule:

DATE	SPEKER	TITLE
26-08.2022	Dr. Koushik Guha Assoc. Prof, NIT Silchar	Recent Trends in CMOS, Emerging Devices and Technology in VLSI Design for Biomedical Applications
27-08.2022	Dr.Balawindar Rai, NITTTR Chandigarh	TFET Devices for Bio-Sensor Applications
28-08.2022	Dr.K.Srinivasa Rao, KL University	Charge-Plasma FET and State of the Art Advanced Semiconductor Devices
29-08.2022	Dr.Suman Latha Tripathy, LPU	Hands-On Session TCAD Tools for Device Simulation
30-08.2022	Dr. Y.Naresh, Malla Reddy University Malla Reddy University	Non classical CMOS devices such as Junction-less FET
1-09.2022	Dr.Keethi Assoc Professor, JNTUH	TCAD Tool for TFET Device Simulation-Hands on Training

Some screenshots of the are as below.



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The screenshot shows a Zoom meeting in progress. The main window displays a presentation slide titled "Historical Perspective" with a bulleted list of historical computing milestones. The slide content is as follows:

- Idea of implementing computational engines with encoded data format is by no means an idea of our times.
- Babbage's Difference Engine - 1832: Large scale mechanical computing device, with decimal numbers.
- Analytical Engine - 1834: A general purpose computing machine with features close modern computers. The complexity and cost of the design made the concept impractical.
- Electrical solution turned out to be more cost effective.
- Earlier digital electronics systems were based on magnetically controlled switched (relay) still used in trains.
- The age of digital electronic computing only started in full with introduction of the vacuum tube.
- A successful commercial computer with 18000 vacuum tubes, 80 feet long, 8.5 feet height.

The Zoom interface includes a top menu bar with options like "File", "Edit", "Share", "View", "Audio & Video", "Participant", "Meeting", and "Help". A toolbar at the bottom allows for actions such as "Unmute", "Start video", "Share", and "Participants". On the right side, there is a chat window with messages from participants, including one from Dr. K. Srikanth Rao stating "Sir, you are audible".

[illegible]

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The screenshot shows a Cisco Webex Meeting interface. The top bar includes 'File', 'Edit', 'Share', 'View', 'Audio & Video', 'Participant', 'Meeting', 'Breakout Sessions', and 'Help'. The main window displays a presentation slide titled 'ATAL AICTE National Workshop on Recent Trends in VLSI Chip/Devices for Sensor Applications' from 26th September to 1st October 2022. The slide also mentions 'Challenges in Analog IC Design in nanoscale CMOS/FinFET technologies' on 29th September 2022, organized by KL University, Guntur. The presenter is Dr. Suman Lata Tripathi, Professor, VLSI Design, School of Electronics and Electrical Engineering, Lovely Professional University, Punjab, India 144622. The right sidebar shows a list of 12 participants, including shoukat vali (Host), Suman Lata Tripathi, ARIVAZHAGAN P, B Tarmila Devi, Bandi Vaisalini, Bismil jafferey, boobalan, Dr. Joshitha C, Dr. K.Srinivasa Rao, drksrao, Girija Sravani Kondaveeti, and Mute all. The bottom bar includes 'Unmute', 'Start video', 'Share', 'Record', and 'Participants'.

For Non Planar Junction

(1) For tunneling through non-planar junctions, it is preferable to construct a complicated polygonal region to use the BBT.NONLOCAL method.

(2) You can achieve this by using several joined QTREGION's. The simple rule that must be followed to join the quadrilaterals is to ensure that point (X2, Y2) of the first quadrilateral matches with (X1, Y1) of the second quadrilateral, and that point (X3, Y3) on the first matches point (X4, Y4) on the next. In this way, you can build up a tunneling mesh to cover quite complicated p-n junction shapes.

(3) You must set up the number of mesh points you require in the tunneling direction and the simplest way to do this is to use the PTS.TUNNEL parameter. The value of PTS.TUNNEL applies to all slices in a region of one or more joined quadrilaterals.

The first QTREGION must have all 4 corner points described.

Fig. Example of a polygonal region made by joining 4 quadrilaterals to cover quite complicated p-n junction shapes

The diagram shows a complex polygonal region divided into four quadrilaterals, labeled 1, 2, 3, and 4. The vertices are labeled with coordinates: (X1, Y1), (X2, Y2), (X3, Y3), and (X4, Y4). The region is defined by a p-n junction (X3, Y3) and a tunneling direction (X2, Y2). The quadrilaterals are numbered 1, 2, 3, and 4, and the vertices are labeled 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100.

ATAL FDP Program on Recent Trends in VLSI Chip/Devices for Sensor Applications

OFF-LINE SPEAKERS:

Prof. Ramesh Vaddi, Associate Professor & HoD, SRM University, Andhra Pradesh



Education

PHD:2011 IIT Roorkee

M.Tech: 2004 **MANIT Bhopal.**

BTech: 2002MVGR College of Engg, Vizianagaram, JNTU Hyderabad

Experience

- 06/2019- Till Date, Associate Professor, Electronics and Communication Engineering | Department, School of Engineering and Applied Sciences, SRM University, Amaravati, Guntur, AP, India
- 06/2018- 05/2019, Senior Research Fellow, School of Computing, National University of Singapore, Singapore.
- 6/2015-05/2018, Assistant Professor, Electronics and Communication Engineering, DSPM International Institute of Information Technology-Naya Rapur Chhattisgarh, India.
- 1/2014-06/2015, Assistant Professor, Electrical Engineering Department, School of Engineering, Shiv Nadar University, Greater Noida, UP, India.
- 2/2013- 08/2013, Postdoc Scholar, Department of Computer Science and Engineering, The Pennsylvania State University, University Park, PA, USA.
- 6/2012-1/2013, Professor, Electronics and Communication Engineering Department, Padmasri Dr. B. V. Raju Institute of Technology (BVRIT), Hyderabad, India.
- 2/2011-4/2012, Postdoctoral Research Fellow, VIRTUS (IC Design Centre of Excellence), Circuits and Systems Division, School of Electrical and Electronic Engineering, Nanyang Technological University, 50 Nanyang Avenue, Singapore.
- 8/2005-12/2007, Assistant Professor, Electronics and Communication Engineering Department, College of Engg, GITAM University, Vizag, India.
- 7/2004-8/2005, Lecturer (Ad-hoc), Electronics and Communication Engineering Department, Maulana Azad National Institute of Technology, Bhopal, India

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Dr.Ch.Pulla Rao,Associate Professor , JNTU K



Education

PHD:JntuK

Mtech:NIT Warangal.

BTech:AMIETE

Experience

May 2015 – Present: Head of the Department ECE
Head of the Department ECE

Devineni Venkata Ramana & Dr. Hima Sekhar MIC College of Technology.

May 2010 - May 2015: Assistant Professor
Assistant Professor
Lakireddy Bali Reddy College of Engineering

Dr.Deepak Kumar Panda,Associate Professor,,School of Electronics and Engineering,VIT-AP.



Education

PHD:2018 NIT,Silchar.

M.Tech: 2014 IIT Kharakpur.

B.Tech: (NIST), Berhampur

Experience

Jul 2010 - Jul 2014: Assistant Professor
Assistant Professor
National Institute of Science and Technology (NIST), Berhampur · Full-time.

May 2018 – Present:Associate Professor
VIT-AP · Full-time

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Dr.A.Narendra Babu,LBRCE, JNTU Kakinada



EDUCATION

PhD: SV University,2008.

PG : SV University,2001.

EXPERIENCE

31-08- 09-present:Professor, LBRCE.

01-07- 2002- 31-08- 2007: Teaching Assistant, SV University.

Dr. Neerai Kumar Misra,Associate Professor (SENSE),Vellore Institute of Technology (VIT)-AP



EDUCATION

PHD: Dr. A.P.J. Abdul Kalam Technical University: Lucknow, Uttar Pradesh, IN ,2017.

M.Tech:Amity ,Lucknow.

BTECH: Integral University: Lucknow, Uttar Pradesh, IN

EXPERIENCE

Jun 2022 – Present: Associate Professor VIT-AP ·

Jan 2018 - May 2022: Associate Professor
Bharat Institute of Engineering & Technology

Jul 2017 - Jan 2018 :Associate Professor
Bharat Institute of Engineering & Technology

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Pranitha, Assoc Professor, JNTU Kakinada



EDUCATION

Pursuing Ph.D in K.L. University, Guntur, Andhra Pradesh

M.Tech - CSE from JNTUK, Kakinada in 2014.

MSc - CSE from S.V. University, Tirupathi in 2002.

EXPERIENCE

2017 – Present- **Assistant Professor** CSE Lakireddy Bali Reddy College of Engineering (Autonomous).

Dr. G. Amarnath, Associate Professor & Associate Dean (R&D) ECE. Marri Laxman Reddy Institute of Technology and Management, Hyderabad



EDUCATION

Ph.D. degree in Microelectronics and VLSI(ECE) in Feb-2019 from NIT-Silchar, India.

M.Tech. degree in VLSI System Design in 2009

B.Tech. degree in ECE in 2007, from Jawaharlal Nehru Technological University, Hyderabad

EXPERIENCE

2019-present: Associate Professor & Associate Dean (R&D)
Department of Electronics & Communication Engineering
Marri Laxman Reddy Institute of Technology and Management,
Hyderabad

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Dr.Y Naresh, ML University



EDUCATION

B.TECH: SV University 2014

M.TECH:JNTUK 2016

PHD:KLEF 2018

EXPERIENCE

2017-present: Associate professor BVC.

Dr.Keethi Assoc Professor. JNTUH



EDUCATION

B.Tech: - Electronics and Communication Engineering, Malla Reddy College of Engineering For Women-Suraram, Hyderabad 2014.

M.Tech: - VLSI System Design, BVRaju Institute of Technology, Narsapur 2016

EXPERIENCE

2017-present: **Dr.Keethi Assoc Professor, JNTUH**

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OFF line Schedule:

DATE	SESSION	SPEKER	TITLE
26-09.2022	FN	Prof. Ramesh Vaddi Associate Professor & HoD, SRM University, AP.	Nano Scale Devices and Circuits For Energy Efficient On-chip VLSI Accelerators for ML and Hardware Security Primitives
26-09.2022	AN	Dr.Ch.Pullu Rao, Associate Professor , JNTU K	Future roadmap Electronics Industry and Recent developments of semiconductor nanoscale devices
27-09.2022	FN	Dr.Deepak Kumar Panda Associate Professor, School of Electronics and Engineering, VIT-AP	TFET based Biosensor for Label-Free Detection of Biomolecules
27-09.2022	AN	Dr.A.Narendra Babu LBRCE, JNTU Kakinada	Charge-Plasma FET and State of the Art Advanced Semiconductor Devices for VLSI design automation
28-09.2022	FN	Dr. Neeraj Kumar Misra Associate Professor School of Electronics Engineering (SENSE), Vellore Institute of Technology (VIT)-AP	Computing and Architecture Beyond CMOS for the Future Electronics Roadmap
28-09.2022	AN	Dr.Pranitha Assoc Professor, JNTU Kakinada	Semiconductor devices and Engineering solutions towards Industry Needs
29-09.2022	FN	Dr. G. Amarnath, Associate Professor & Associate Dean (R&D) Department of Electronics & Communication Engineering Marri Laxman Reddy Institute of Technology and Management, Hyderabad	GaN HEMTs and its Modeling for RF and Power Electronic Aplications
29-09.2022	AN	Dr.Y Naresh, ML University	"TCAD-Circuit simulation by Synopsys"

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Second one Week OFFLINE Course Sessions ()

Day-1 (26/10/2022)

Session-1: Morning Session: We have called **Dr.Ramesh Vaddi** from **SRM University** for first day morning session and he gave a talk "Nanoscale Devices and Circuits for Energy Efficient on Chip VLSI Accelerators for ML and Hardware Security Primitives" .

We have Lunch Session, arranged at SAC, R&D Building Ground Floor for 60+ Members from Department of ECE, HoD, Deans, RPACs, Technical Members etc.

Industrial Visit Session.

Session-2: Afternoon session we have **Dr.Ch.Pulla Rao**, given talk on " **Future roadmap Electronics Industry and Recent developments of semiconductor nanoscale devices**".

Day-2 (27/10/2022)

Session-3: **Dr.Deepak Kumar Panda** from **VIT University** has given a talk on "**TFET baswed Biosensors for labelfree detection of biomolecules**". He has given clear understanding about TFET devices and its issues and possible solutions.

We have Lunch Session, arranged at SAC, R&D Building Ground Floor for 60+ Members from Department of ECE, HoD, Deans, RPACs, Technical Members etc.

Industrial Visit Session.

Session-4: **Dr.A.Narendra Babu**, LBRCE, JNTU Kakinada has given a talk on " **Charge-Plasma FET and State of the Art Advanced Semiconductor Devices for VLSI design automation**".

Day-3 (28/10/2022)

Session-5: **Dr.Neeraj KUMar Mishra** from **VIT University** given a talk on "**Computing and Architecture beyond CMOS for the future Electronics rodmap**". She has focus on TFET devices, particularly Charge Plasma FET for Bio-Sensing applications.

We have Lunch Session, arranged at SAC, R&D Building Ground Floor for 60+ Members from Department of ECE, HoD, Deans, RPACs, Technical Memberes etc.

Industrial Visit Session.

Session-6: **Dr.Pranitha**, JNTU Kakinada has given a talk and hands on session "**Semiconductor devices and Engineering solutions towards Industry Needs**".

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Day-4 (29/10/2022)

Session-7: **Dr.G Amarnath** from **MLRIT** has given a talk on "**GaN HEMTs and its modelling for RF and Power Electronic Applications**". He has talk about present state of all kind of VLSI Chip Devices and its applications.

We have Lunch Session, arranged at SAC, R&D Building Ground Floor for 60+ Members from Department of ECE, HoD, Deans, RPACs, Technical Members etc.

Session-8: **Dr.Y Naresh**, **ML University** has given hands on session "TCAD-Circuit simulation by Synopsys".

Session-9: Last Session is talk given by **Dr.Keethi**, **JNTUH** on "TCAD Tool for TFET Device Simulation-Hands on Training".

The total number of participants for this workshop is 50 members; they have attended from around the state, few members from out of state too. The organizing committee has done excellent job, without any discrepancy and disturbance in any aspects.. Finally, as Convener **Dr.K.Srinivasa Rao** and Co-Convenors says vote of thanks to resources, & participants for making the workshop a grand success.

SOME Photos Of The Sessions:



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Participants:

Name	Application_Number	Institute Name	Designation
Mrs. Bandi Vaisalini	1649329521	Tirumala Engineering College	Assistant Professor
Mr. Shaik Azad Basha	1649329521	Acharya Nagarjuna University, Guntur	Post graduate student
Dr. B.Balaji	1649329521	Klef	Associate Professor
Dr. Neeraj Kumar Misra	1649329521	Vellore Institute Of Technology -Ap University Amaravati, India	Associate Professor
Dr. Lakshman Pappula	1649329521	Klef (Deemed To Be University)	Associate Professor
Dr. Suman Lata Tripathi	1649329521	Lovely Professional University	Professor
Dr. Fazal Noorbasha	1649329521	K L University	Associate Professor
Dr. . Hari Kishore Kakarla	1649329521	Klef (Deemed To Be University)	Professor
Mr. Arivazhagan P	1649329521	Sir Issac Newton College Of Engineering And Technology	Assistant Professor
Mr. Meena Nagaraju	1649329521	Velagapudi Ramakrishna Siddhartha Engineering College	Assistant Professor
Dr. Badugu Suresh	1649329521	Koneru Lakshmaiah Education Foundation	Associate Professor
Dr. Ghali Venkata Subbarao	1649329521	Klef	Professor
Mr. Girish Pechetti	1649329521	Bvc Institute Of Technology And Science,Amalapuram	Assistant Professor
Mrs. Divya S	1649329521	Sree Narayana Gurukulam College Of Engineering	Assistant Professor
Dr. Vullanki Rajesh	1649329521	KI Deemed To Be University	Professor
Mrs. Naga Lakshmi Balla	1649329521	Bvc Engineering College Odalarevu	Assistant Professor

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Mrs. Shalini M	1649329521	Prince Shri Venkateshwara Padmavathy Engineering College	Assistant Professor
Dr. Girija Sravani K	1649329521	Koneru Lakshmaiah Education Foundation	Assistant Professor
Mr. Syed Shameem	1649329521	KI University	Associate Professor
Dr. Kolla Bhanu Prakash	1649329521	Koneru Lakshmaiah Education Foundation	Professor
Dr. Arunmetha S	1649329521	Klef	Associate Professor
Miss Harika Pangam	1649329521	Bonam Venkata Chalamayya Engineering College	Associate Professor
Mr. Inukonda Rama Satya Nageswara Rao	1649329521	Bonam Venkata Chalamayya Engineering College (A)	Assistant Professor
Dr. Joshitha C	1649329521	Klef	Associate Professor
Dr. S. Koteswara Rao	1649329521	Koneru Lakshmaiah Education Foundation	Professor
Dr. Anup Dey	1649329521	Malla Reddy College Of Engineering And Technology (Autonomous-Ugc, Govt. Of India)	Assistant Professor
Mr. R Kiran Kumar	1649329521	Madanapalle Institute Of Technology Science	Assistant Professor
Mr. K. Lakshmi Narayana	1649329521	Rgukt Srikakulam	Assistant Professor
Mrs. Rajeswari. P	1649329521	Dayananda Sagar College Of Engineering	Assistant Professor
Mr. Bismil Jaffery	1649329521	Narayan	Assistant Professor
Mrs. Tarmila Devi.Bollavarapu	1649329521	P.B.Siddhartha College Of Arts & Science	lecturer
Dr. Jaya J	1649329521	Hindusthan College Of Engineering And Technology	Professor

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Mr. Kunal Kishor	1649329521	Vpm's Mpcoe Velneshwar	Assistant Professor
Mr. Ranjeet Ramesh Suryawanshi	1649329521	Bharati Vidyapeeth's College Of Engineering, Kolhapur	Assistant Professor
Mr. Boobalan D	1649329521	Karpagam Academy Of Higher Education	Post Graduate student
Dr. Manojkumar S B	1649329521	Bgs Institute Of Technology	Associate Professor
Mrs. Vinotheni M S	1649329521	Madras Institute Of Technology	TEACHING FELLOW
Mrs. Vishakha Sandeep Kharotechavan	1649329521	Pimprichinchwad Polytechnic,Pune	Assistant Professor
Mr. Snehith Namgiri	1649329521	Koneru Lakshmaiah Education Foundation	Scholar
Mrs. E N Srivani	1649329521	Sjcit	Assistant Professor
Mrs. Anitha C	1649329521	S J C Institute Of Technology	Assistant Professor
Mrs. Manjula K	1649329521	S J C Institute Of Technology	Assistant Professor
Dr. Srinivasa Rao K	1649329521	KI University	Professor
Dr. Sreevardhan Cheerla	1649329521	K L University	Associate Professor
Mr. S Vamsee Krishna	1649329521	K L (Deemed To Be) University	Assistant Professor
Mr. Shaik Shoukat Vali	1649329521	Kluniversity	Research scholar
Dr. Suman Maloji	1649329521	Klef	Professor
Mr. Pamarthi Naga Basaveswara Swamy	1649329521	Nri Institute Of Technology	Assistant Professor