

KONERU LAKSHMAIAH EDUCATION FOUNDATION

Examination Branch

I M.Tech I Semester Regular Examination Results December - 2018 (Y18 Batch)

Very Large Scale Integration

			L	T	P	Cr			L	T	P	Cr		L	T	P	Cr	L	T	P	Cr	L	T	P	Cr	L	T	P	Cr	L	T	P	Cr
			3	1	2	5			3	1	2	5		3	1	0	4	3	1	0	4	3	0	0	3	3	0	0	3	0	0	4	2
S. NO.	REGISTER NO.	18EC5130 HDL AND PLD ARCHITECTURE						18EC5128 MOS CIRCUIT DESIGN						18EC5131 IC FABRICATION TECHNOLOGY				18EC5129 ALGORITHM FOR VLSI DESIGN AUTOMATION				18EC51R4 MEMORY DESIGN AND TESTING				18EC51Q1 EMBEDDED SYSTEM DESIGN				18IE5149 Seminar			
		TIM 60	ELM 40	ETM 40	TEM	GT	GRADE	TIM 60	ELM 40	ETM 40	TEM	GT	GRADE	TIM 60	ETM 40	GT	GRADE	TIM 60	ETM 40	GT	GRADE	TIM 60	ETM 40	GT	GRADE	TIM 60	ETM 40	GT	GRADE	TIM 60	ELM 40	GT	GRADE
1	182042001						A+						A+				A				A+	48	AB	AB	AB				B+				A+
2	182042002						A						A				A				A+				A				B+				A
3	182042003						A+						A				A				A+				A				A				A+
4	182042004						A						B+				A				A				A+				B				A
5	182042005						A+						A				A				A+				A+				B+				A
6	182042006						B+						B				A				B+				B+				B				B+
7	182042007						A+						A+				A+				A+				A+				A				A+
8	182042008						A						B+				A				A				A				B				A

CHECKED BY

CONTROLLER OF EXAMINATIONS