

## K L E F (Deemed to be University)

### Department of Electronics and Communication Engineering

#### Alumni Guest Lecture Report

**DT: 15-03-2021**

**Resource Person:** Mr.K.Harikrishna (Reg.No:13204110)  
Project Lead, Synopsis, Bangalore

**Topic:** Analog VLSI Design and Its Applications

**Audience: STUDENTS**

**Time: 11:30AM-12:30PM**

#### Contents covered:

An Alumni guest lecture was conducted on the topic “Analog VLSI Design and Its Applications” on 15-03-2021 at 11:30AM to 12:30PM. Speaker- **Mr.K.Harikrishna** was a Project Lead, Synopsis, Bangalore. He gave an Introduction of synthesis of digital circuits in Analog VLSI design. Synthesis of circuit is defined as a process of generating net list from a circuit design model. Synthesis means ‘to generate’. It is a step to generate circuit hardware schematics. VLSI design flow is revisited here explaining the role of synthesis in digital design flow. In VHDL design flow, synthesis is performed for hardware generation after simulation verifies the logic functionality of circuit under design and is ready for its hardware.



Alumni Coordinator

(Dr. S.Nagendram&A.Madhuri)

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