

Koneru Lakshmaiah Education Foundation

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Workshop on ASIC design

Date: 05/01/2022

Department Name	: ECE
Event Date(s)	: 05/01/2022
Event Time	: 03.00 Pm to 05.00 pm.
Event Name	: Design and architecture of FPGA
Event Category	: Department Association Activity-IEEE EDS
Event Objective	: To design and develop ASIC and architecture of FPGA
Event Venue / Room No.	: R106
Faculty Coordinator EMP.ID	: 5155
Faculty Coordinator Name	: Dr.B.Balaji, Associate Professor, ECE
Faculty Coordinator Email-ID	: <u>balaji@kluniversity.in</u>

Student Organizers : S.Rohini

The following KLEF students have attended

No. of Volunteers Participated: 12

No. of faculty Participated: 02

S.No	Reg.No	Name of the Student	Signature
1	2200040293	SAMPATHI ROHINI	Rohimi
2	2200049043	GUNTAMUKKALA NIKHITA .	euleh:
3	2200049094	TADISETTY NAGA NITISH KUMAR	NETER KLIMAN
4	2100049101	SINGAMREDDY VISHNU VARDHAN REDDY	Vashmu
5	2100040046	MURARI SRI HARI	Srihond
6	2100049026	JANGA MANOJ KUMAR REDDY	Mahan Miman
7	2100049037	PAMARTHI RAVI TEJA	pail TOS Q
8	2100040094	BATTULA KESAVA SRINIVAS	Spinimus

9	2100040263	UDUMULA POOJITHA	Poolitha
10	2100040156	MEKALA CHANDRA SEKHAR	Chandrasekhar
11	2100040208	GOTTAPU BHARGAVA RAO	Bharrous Rac)
12	2100040219	SUNKESULA VIJAY KUMAR	No2





Event Outcome:

The Students have learnt the basic design of ASIC and architecture of FPGA under IEEE EDS Program for its specific application converge towards advancing semiconductor technology, improving device performance, enhancing reliability, and driving innovation in various applications ranging from consumer electronics to healthcare and automotive systems.

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Dr.B.Balaji Signature of Faculty Coordinator

Dr.M.Suman,

Head of the Department. DECE NEL University Professor & Head Department of ECE K L E F Green Fields, Vaddeswaran.

