



Koneru Lakshmaiah Education Foundation

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Campus: Green Fields, Vaddeswaram, Guntur District, A.P., India. PIN 522 302

Phone No. 08645 - 350200; www.kluniversity.in, www.klef.edu.in

National Webinar on ASIC Design Flow

Date: 06/01/2021

Department Name : ECE
Event Date(s) : 06/01/2021
Event Time : 03.00Pm-04.00PM
Event Name : National Webinar on ASIC Design Flow
Event Category : **Department Association Bodies**

Event Objective : The objective of the ASIC (Application-Specific Integrated Circuit) Design Flow event is to provide participants with a comprehensive understanding of the various stages, methodologies, and best practices involved in the design and development of ASICs. The event will cover the fundamental concepts and principles of ASIC design, including architecture exploration, specification definition, and design partitioning. Participants will gain insights into the key components of ASICs and the role they play in system integration. Participants will delve into different ASIC design methodologies and flows, such as RTL (Register Transfer Level) design, logic synthesis, and physical design. Through expert-led presentations and interactive sessions, attendees will learn about the sequential stages of ASIC design and the tools and techniques used at each stage.

Resource Person : **Mr. Jagadeesh Pujar.**

Event Venue / Room No. : **Online**

Faculty Coordinator EMP.ID : **5155**

Faculty Coordinator Name : **Dr.B.Balaji Associate Professor, ECE**

Faculty Coordinator Email-ID: balaji@kluniversity.in

The following KLEF students have attended

No. of Volunteers Participated: 32

No. of faculty Participated: 02

S.No	Reg.No	Name of the Student
1	190040410	PONNADI RAJENDRA
2	190040411	POTHANI PRABHUKIRAN
3	190040415	PARASARAM SUSEEL KOUSIC
4	190040449	SAMIRAJU SAI BHANUTEJA
5	190040468	SHAIK GOUSE RABBANI
6	2100040021	GOGINENI RUTHVIK
7	2100040022	MACHERLA DURGA SAI DEEPAK
8	190040221	KATRAGADDA SATYAKEERTHANA
9	190040231	KILARU PAVAN SAI
10	190040438	REVVU CHANIKYA RAJU
11	190040216	KARUMURI DHANESH
12	190040585	YELLAPRAGADA SRI VENU KAMAL
13	190040089	CHIKKARAJU ARUN
14	190040104	DANDA SAI VIJAY
15	190040460	SHAIK HAMEED BASHA
16	190040111	SAI SATYA SIVA VAMSI KRISHNA DASARI
17	190040117	DEVARAPALLI PAVAN CHARITH
18	190040125	DONTHI REDDY GNANA PRAKASH REDDY
19	190040140	GAVINI RUPA RANI
20	190040144	GOGINENI VASUDEV
21	190040153	GORRE VENKATA HARI PRASAD
22	190040156	GOURIPEDDI PAVANI SOWMYA
23	190040173	GURRAM RISHITHA
24	190040187	JASHTI GOPI NATH
25	190040194	KALAPALA THANUSHA
26	190040201	KANAKALA SATYA VENKATA AKASH
27	190040204	KANKANALA SAI ANURAG
28	190040216	KARUMURI DHANESH
29	190040585	YELLAPRAGADA SRI VENU KAMAL
30	190040591	YERUVA NITESH KUMAR REDDY
31	190049024	GANAPAVARAPU VEDAVYAS
32	190049025	RAVURI ANUSHA



KL
(DEEMED TO BE UNIVERSITY)

Electronics & Communication Engineering



A National Online Webinar on **ASIC DESIGN FLOW : INDUSTRY PERSPECTIVES**

6th January, 2021 @ 3:00 pm - 4:00 pm

Organized by : **Micro Electronics Research Group (MERG), KL**



Resource Person:
Mr. Jagadeesh Pujar
Sr. Product Development Engineer
Global Foundries Pvt. Ltd, Bangalore.

General Chair :
Dr. Vinay Kumar Mittal
Dept. of ECE, KL

Chair :
Dr. M. Suman
HOD, Dept. of ECE, KL

Technical Chair :
Dr. K. Srinivasa Rao
Professor & Head MERG Research Group, KL

Convenor :
Mr. M. Aditya
Assistant Professor, KL

Co-Convenors :
Dr. B. Balaji
Associate Professor, KL
Dr. K. Girija Sravani
Assistant Professor, KL

For Details Contact:
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+91 - 7095666645

E-Mail:
aditya@kluniversity.in
balaji@kluniversity.in
kondavitee.sravani@kluniversity.in

Registration Link:
<https://forms.gle/pAEVU213gT8iXKzV7>

Description: The ASIC (Application-Specific Integrated Circuit) Design Flow workshop provides participants with a comprehensive overview of the process involved in designing custom integrated circuits tailored to specific applications. Tailored for engineers, designers, and professionals involved in ASIC design, this workshop covers the entire design flow, from concept to tape-out, ensuring attendees gain practical insights and essential skills for successful ASIC development. Participants will receive an introduction to ASIC design principles, including architecture exploration, specification definition, and design partitioning. Through interactive sessions and expert-led presentations, attendees will grasp the foundational concepts of ASIC design and its importance in system integration.

The workshop delves into various ASIC design methodologies and flows, including RTL (Register Transfer Level) design, logic synthesis, and physical design. Attendees will gain a comprehensive understanding of the sequential stages involved in ASIC design and the tools and techniques utilized throughout the process. Advanced topics such as low-power design techniques, design for testability (DFT), and timing closure will be covered in-depth. Participants will explore the challenges and considerations associated with designing complex ASICs for high-performance and low-power applications. Industry experts will showcase state-of-the-art ASIC design tools and technologies, providing attendees with hands-on experience and practical insights into their capabilities. Through demonstrations and interactive sessions, participants will learn about the latest advancements in ASIC design automation, verification, and physical implementation.

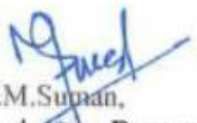
Event Outcome:

The ASIC Design Flow workshop was highly successful in empowering participants with the knowledge, skills, and resources needed to navigate the intricacies of ASIC design effectively. By providing practical insights, hands-on experience, and networking opportunities, the workshop has made a significant contribution to advancing the capabilities of ASIC designers and promoting excellence in ASIC development.



Dr.B.Balaji

Signature of Faculty Coordinator



Dr.M.Suman,
Head of the Department.
DECE, KL University
DR. M. SUMAN
Professor & Head
Department of ECE
K L E F
Green Fields, Vaddeswaran.
Tuntur Dist., A.P. PIN: 522 507



National Webinar on "ASIC Design Flow: Industry Perspectives" by Microelectronics Research Group, Department of ECE, KLEF – Reg.

Registrar <registrar@kluniversity.in>

Sat 1/2/2021 12:36 PM

To: KLU Chancellor <chancellor@kluniversity.in>; PRESIDENT <president@kluniversity.in>; Havish <havish@kluniversity.in>; Raja H Koneru <krh@kluniversity.in>; konerurajaharin@gmail.com <konerurajaharin@gmail.com>; Dr. S S Mantha <ssmantha@kluniversity.in>; ssmantha33@gmail.com <ssmantha33@gmail.com>; Dr. Subrahmanyam <smkodukula@kluniversity.in>; Chancellor Office <chancelloroffice@kluniversity.in>; Dr.Venkat <drvenkat@kluniversity.in>; Pro Chancellor Office <prochancelloroffice@kluniversity.in>; Vice Chancellor - KLU <vc@kluniversity.in>; Dr. LSS Reddy <drlessreddy@kluniversity.in>; N Venkat Ram <venkatram@kluniversity.in>; Office Of Pro-VC <provcoffice@kluniversity.in>; Registrar <registrar@kluniversity.in>; Dr Y V S S V Prasada Rao <prasadarao_yvssv@kluniversity.in>; Dr Jagadeesh Anne <drjagadeesh@kluniversity.in>; kamineni jayanendra babu <kjbabu_ipe@kluniversity.in>; sekhar <sekharbabu@kluniversity.in>

Ref: KLEF/RO/HOD-ECE/2020-21

Date: 02-01-2020

Orders of the Vice-Chancellor dt.02-01-2020

CIRCULAR

Sub: National Webinar on "ASIC Design Flow: Industry Perspectives" by Microelectronics Research Group, Department of ECE, KLEF – Reg.

Ref: Letter dated 01.01.2021 from Dr.K. Srinivasa Rao, Microelectronics Research Group Head forwarded by Dr.P. Lakshman,Dy. HoD-ECE.

This is to inform all the students and faculty members that Microelectronics Research Group, Department of ECE, KLEF, is conducting a National webinar on "**ASIC Design Flow: Industry Perspectives**" as per the details given below.

All students and faculty members can register for the events.

Date: 6th January 2021 Time: 3.00 p.m. to 4.00 p.m.

Registration Link: <https://forms.gle/pAEVU213gT8iXKzV7>

Chair : Dr.M. Suman, Professor & HOD, Department of ECE, KLEF
Technical Chair: Dr.K.Srinivasa Rao, Professor, Department of ECE, KLEF
Convenor : Mr.M.Aditya, Assistant Professor, Department of ECE, KLEF
Co-Convenors : Dr.B.Balaji & Dr.K.Girija Sravani, Department of ECE, KLEF

Poster of the webinar is enclosed herewith.

REGISTRAR

Encl: Poster

Mail & Hard copy to: Hon'ble President, KLEF

Mail to: Hon'ble Vice-Presidents,

KLEF

Mail & Hard copy to: Hon'ble Chancellor / Hon'ble Pro Chancellor / Hon'ble Vice-Chancellor

Mail & Hard copy to: OSD to Hon'ble Chancellor-Dr.K. Subrahmanyam

Mail & Hard copy to: Pro Vice-Chancellor (Administration)-Dr.N.Venkatram

Mail to: Chief Coordinating Officer-Dr.A. Jagadeesh / Chief Coordinating Officer of Examinations-Dr.K.J.Babu

Mail to: Special Officer -Dr.A. Vani, / Special Officer (Academic Audit) - Dr.A.Anand Kumar / Special Officer (Research Audit) & Head (Research Consultancy & Smart Campus)-Dr.Vinay Kumar Mittal

Mail to: Advisor-Quality for KLU and In-charge of Hyderabad Off-campus & Administrative Office - Prof. K.Koteswara Rao

Mail to: All Advisors / All Deans / All Principals / All Directors / Additional Dean / All Associate Deans / Deputy Deans

Mail to: Controller of Examinations-Dr.A.S.C.S.Sastry

Mail to: Chief Financial Officer / Finance Manager-Mr.KRR / Manager (Accounts)-Mr.B.Mallikharjuna Prasad

Mail to: Deputy Registrars-Dr.B Sekhar Babu & Sri S Vijaya Babu / Assistant Registrars-Sri A Krishna Rao & Sri K Vara Prasad

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Mail to: Vice-Principal-Coll. of Science & Humanities & Coordinator-FED..Dr.VKR/Vice-Principal-Acad. Staff College-Dr.BSiva Nagaiah

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HoDs..Maths / PHY / CHEM / ENG / BES-I / BES-II

HoDs..MBA / BBA / COM / HM / CSS / CA&MS / Law / Architecture / Pharmacy / BCA / Arts

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Mail to: GM-Mr.YSRKP/Jt.Registrar-A.O.-Mr.C.S.Rao/Director-Adm-Dr.J.S.Rao / Media I/c & PRO - Mr.HSR Murty

Mail to: Manager (HR)-Mr.J. Sreekanth-A.O./Manager (HR)-Campus..Mr.SPSN Srinivas/ Pay Bills Section-A.O.

Mail to: Head-HR, KL H

Mail to: Manager (EduTech, Animation & Publishing)-Mr.Shakthi Swaroop _

Mail to: In-charge, Technical Maintenance / Hobby Clubs & Stud. Activities (KLUSO)-Dr.R Subhakara Raju

Mail to: Webmaster _

Mail to: Physical Edn. /Library / Gen.Maintenance / Transport / Construction / Central Stores / Girls' Hostel/

Boys' Hostel / Exam.Sec / Automation / SyTe / ET S&A Gr // IQAC / VEC / CAES / ASAS /

/Intl Relations / Hobby Clubs / Help Desk / PIPS / Women's Forum / Elecl.Wing / Security / Canteen

Mail to: All faculty

Mail to: All students

Thanks & Regards



Prof. Y. V. S. S. V. Prasada Rao
Ph.D(Mech. Engg.), DPM., MBA (Fin & HR), FICWA
REGISTRAR

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