



**Koneru Lakshmaiah Education Foundation**  
(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

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Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.  
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**Ref: KLEF/RO/ECE/CIRCULAR**

**Date: 18-09-2026**

**CIRCULAR**

**Sub:** Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.

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This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Introduction to Next Generation PCB Design (Modern UX)" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 22-09-2026, as details below:

Event Name: "Workshop"

Date: 22-09-2026

Venue: R-106

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

  
HOD-ECE

To  
All ECE Students,  
All ECE Faculty,  
Principal.



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## **A One-Day Workshop On “Introduction to Next Generation PCB Design (Modern UX)”.**

**By**

**Always@VLSI**

**Department Of ECE**

**Name of the event:** Introduction to Next Generation PCB Design (Modern UX)

**Dates:** 22-09-2026

**Venue:** R106

**No. of students participated:** 25

### **Objective of the event:**

The workshop “Introduction to Next Generation PCB Design (Modern UX)” organized by the Always@VLSI Club is aimed at introducing participants to modern printed circuit board (PCB) design methodologies and advanced design workflows. The workshop focuses on understanding the evolution of PCB design from traditional approaches to next-generation, user-friendly design environments that support high-speed, high-density, and complex electronic systems. Participants are expected to learn the fundamentals of PCB design, including schematic development, component placement, routing, signal integrity, power integrity, design rule checking, and design optimization. The workshop also provides an overview of modern EDA tools and workflows used in industry for efficient PCB development, with emphasis on Siemens EDA solutions such as Xpedition and HyperLynx. Through demonstrations and practical exposure, participants can understand how modern PCB design practices contribute to reliable, high-performance electronic products.

### **Description of the event:**

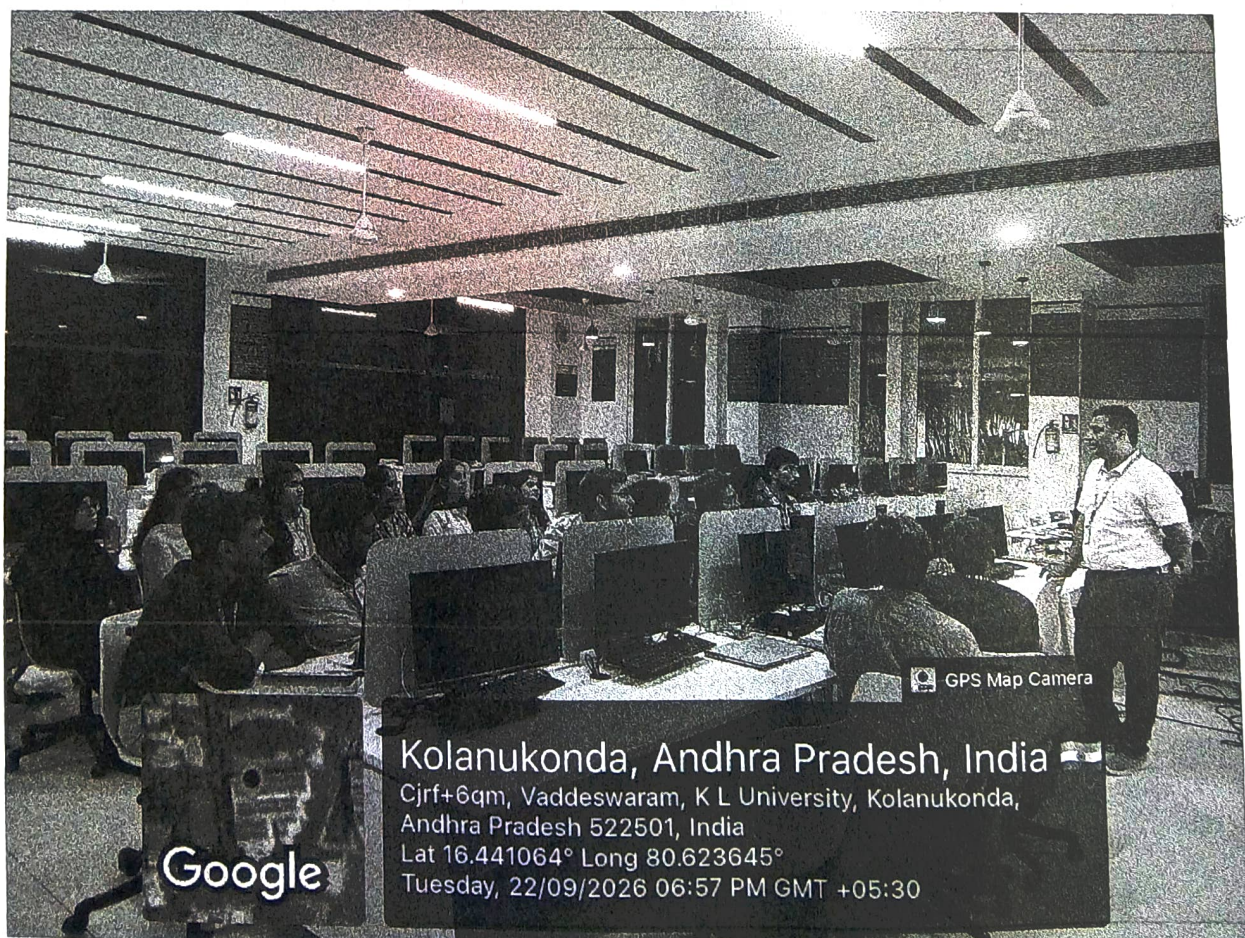
The “Introduction to Next Generation PCB Design (Modern UX)” workshop organized by the Always@VLSI Club is a comprehensive technical session designed for students and

enthusiasts interested in learning contemporary PCB design techniques and industry-oriented electronic design workflows. The workshop introduces participants to the complete PCB design flow, beginning with schematic capture and component management and progressing through placement, routing, verification, and design analysis. Special emphasis is given to modern user experience (UX) features that simplify PCB design, improve productivity, and support collaborative engineering workflows. Participants are introduced to important concepts such as high-speed signal routing, signal integrity, power integrity, electromagnetic interference and compatibility (EMI/EMC), transmission-line effects, ringing, ground bounce, TDR, and eye-diagram analysis. Demonstrations using Siemens EDA Xpedition and HyperLynx help participants understand how advanced PCB design and analysis tools are applied to real-world electronic systems. The combination of conceptual explanations, tool demonstrations, and practical examples enables participants to connect theoretical knowledge with current industry practices.

### **Outcome of the event:**

The “Introduction to Next Generation PCB Design (Modern UX)” workshop provided participants with valuable exposure to modern PCB design methodologies, advanced EDA workflows, and industry-oriented design practices. Participants developed an understanding of the complete PCB design process, including schematic development, component placement, routing, design verification, and performance analysis. The workshop enhanced their awareness of critical high-speed design considerations such as signal integrity, power integrity, EMI/EMC, transmission-line effects, ringing, ground bounce, TDR, and eye-diagram analysis. Through demonstrations of Siemens EDA Xpedition and HyperLynx, participants gained practical insight into how professional PCB design and simulation tools are used to identify and resolve design issues before hardware fabrication. The event also strengthened participants’ understanding of modern PCB design UX concepts, improving their ability to approach complex electronic design challenges systematically. By the conclusion of the workshop, participants gained enhanced technical knowledge, practical awareness, and industry-relevant skills that can support further learning, academic projects, research activities, and career opportunities in PCB design and electronic system development.

## Photos of the event:



## Participants List:

Sl. No. Name of the student

- 1 X. Harishvardhan
- 2 T. Radha Venkat
- 3 M. Sai Nihhar
- 4 M. Abhiram Monikanta Reddy
- 5 B. Uday Kumar
- 6 M. Jyoti
- 7 S. Eshwari Vardan
- 8 N. Shashikiran Reddy
- 9 B. Tanish Raj
- 10 P. Pavani Kumar
- 11 Pandu Ranga Varun
- 12 M. Mokshagna
- 13 B. Monikanta
- 14 M. Umasteer
- 15 A. Sudheesh
- 16 V. Hansika
- 17 Afiahk
- 18 S. Vaishnavi
- 19 B. Swanya Bharathi
- 20 K. Krishnaveni
- 21 P. Sai Sivi Varshini
- 22 U. Sowjanya
- 23 D. Ruchitha Naidu
- 24 B. Jahnvi
- 25 N. Pavani Priya

X. Harishvardhan

T. Radha Venkat

M. Sai Nihhar  
M. A. Monikanta Reddy

B. Uday Kumar

M. Jyoti

Eswari Vardan

N. Shashiki

Tanish

Pavani

Varun  
Mokshagna

Monikanta

Umasteer

Sudheesh

Hansika

Afiahk

Vaishnavi

Swanya

Krishnaveni

Varshini

Sowjanya

D. Ruchitha Naidu

Jahnvi

N. Pavani

S. V. V. V.  
Incharge-Always@VLSI

HOD-ECE 25/09/24