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#### Ref: KLEF/RO/ECE/CIRCULAR

#### Date: 10-02-2021

#### **CIRCULAR**

**Sub:** Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.

This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Workshop on Opportunities and challenges in VLSI Front End Design and Verification" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 12-02-2021, as details below:

| Event Name: "Workshop" |            |  |
|------------------------|------------|--|
| Date:                  | 12-02-2021 |  |
| Venue:                 | R-106      |  |

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

HOD-ECE Professor & Allernate T. Department of ECE

Department of ECE K L University VADDESWARAM Guntur Dt., A.P., India.

To All ECE Students, All ECE Faculty, Principal.



Koneru Lakshmaiah Education Foundation (Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956) Accredited by NAAC as 'A++' & Approved by AICTE & ISO 9001-2015 Certified Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA. Phone No. 08645 - 350200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129.

## Expert talk on "Opportunities and challenges in VLSI Front End Design and Verification"

By

## Always@VLSI

## **Department Of ECE**

Name of the event:Opportunities and challenges in VLSI Front End Design and Verification

Dates:12-02-2021

Venue:Online/Offline

## No. of students participated: 111

## **Objective of the event:**

Always@vlsi club hosted an expert talk on "Opportunities and Challenges in VLSI Front End Design and Verification," offering attendees valuable insights into the intricacies of this critical aspect of semiconductor development. The session delved into the current landscape of VLSI (Very Large Scale Integration) front-end design and verification, highlighting emerging opportunities and addressing prevalent challenges in the field. Through in-depth discussions led by industry experts, participants gained a comprehensive understanding of the latest trends, methodologies, and best practices in VLSI front-end design and verification. Additionally, the talk provided a platform for attendees to engage with professionals, exchange ideas, and gather practical advice to navigate the complexities of front-end design and professional development, empowering participants to excel in the dynamic realm of VLSI design and verification.

#### **Description of the event:**

This expert talk focusing on "Opportunities and Challenges in VLSI Front End Design and Verification." This insightful session aimed to provide attendees with a comprehensive understanding of the intricacies involved in front-end design and verification within the VLSI domain. Industry experts shared valuable insights into the current landscape of VLSI front-end design, highlighting emerging opportunities and addressing prevalent challenges faced by professionals in this field. Topics covered included the latest trends, methodologies, and best practices in front-end design and verification, offering attendees practical knowledge to enhance their skills and stay competitive in the industry. Through engaging discussions and interactive Q&A sessions, participants had the opportunity to gain valuable insights, network with peers, and gather practical advice from experienced professionals. Overall, the expert talk served as a platform for knowledge exchange and professional development, empowering attendees to navigate the complexities of VLSI front-end design and verification with confidence.

### **Outcome of the event:**

The expert talk on "Opportunities and Challenges in VLSI Front End Design and Verification," organized by the Always@vlsi club, yielded significant outcomes for participants. Attendees gained a deeper understanding of the complex landscape of VLSI front-end design and verification, including emerging opportunities and prevalent challenges within the industry. Through engaging discussions and presentations by seasoned professionals, participants acquired valuable insights into the latest methodologies, tools, and best practices employed in front-end design projects. Moreover, the event provided a platform for networking and collaboration, allowing attendees to connect with peers and industry experts, fostering potential partnerships and knowledge exchange. Armed with newfound knowledge and insights, participants were better equipped to tackle the challenges and leverage the opportunities in VLSI front-end design and verification, ultimately enhancing their skills and competitiveness in the field.

## Photos of the event:



In charge describing the event description

# Participant's List:

|     | Name of                   |                           |        |               |
|-----|---------------------------|---------------------------|--------|---------------|
| Na  | the Student               | Register Number           | Branch | Signature     |
| 1.  | 180049020                 | SHAIK THAHERUNNISA        | ECE    | S.Thuman      |
| 2.  | Consecutive -             | SINGAMSETTY LOHITH NAGA   | ECE    | Q.C.          |
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| 5.  | 180049009                 | KILARU SAIPRASANTH        | ECE    | Page          |
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| -   | 180049008                 | PHANEENDRA MANIKUMAR      |        | Depart.       |
| 7.  | 180049007                 | IRRINKI NAGA DURGA RAJESH | ECE    | I.N.D. And    |
| 8.  | 180049003                 | B SHIVA KUMAR             | ECE    | B. Libathe    |
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| 11. | 180040737                 | KADALI SAI SNEHA          | ECE    | K. Shela      |
| 12. | 180040726                 | MADENENI VENKAT CHANDU    | ECE    | M. V. chande  |
| 13. | 180040721                 | MADHURI TAMMA             | ECE    | M. Tama       |
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| 19, |                           | MANNEPALLI VENKATA        | ECE    | num           |
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In charge Always@VLSI Technical Club HOD-ECE Professor & Alternate , Department of ECE K L University VADDESWARAM Guntur Dt., A.P., India