

K L UNIVERSITY
DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING
Guest lecture with Industry and Academic Expert on
“Modeling challenges in state-of-art VLSI devices: FinFETs and Multi-gate devices”

The **DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING (ECM)** K.L.University, Vaddeswaram organized an expert lecture cum interactive secession on “**Modeling challenges in state-of-art VLSI devices: FinFETs and Multi-gate devices**” on 25-02-15. Three hundred students from UG and PG courses of ECM/ECE/EEE departments participated in the secession.

The resource person Dr. Srivatsava Jandhyala, worked as “ASIC Physical Design Engineer “ in semiconductor companies such as Broadcom, Freescale and Intel and presently working at Centre for VLSI and Embedded System Technologies (CVEST), IIIT, Hyderabad. He emphasized the importance of the FinFETS and Multi gate devices their advantages and challenges in the Modeling of high performance device with less than 22nm technology. The need for new generation Architectures with more miniaturization, power optimization and speed enhancement to meet the feature electronic industry demands are discussed in detail. Further the students are made ware of different tools used in the industry used for modeling of these architectures. He also focused on the different job opportunities and advanced research areas in the VLSI domain in near future and need for the Industry and Institute interaction in making effective future Design Engineers for Electronic Industry. Dr. M.Suman HOD, ECM department, Dr. K.Sreenivasa Ravi Professor and other faculty of the ECM/ECE departments participated in the Guest Lecture.



Dr. Srivatsava Jandhyala addressing the Students and Faculty



Dr. Dr. Srivatsava Jandhyala giving Key Note talk on the event.

**Dr. K.Sreenivasa Ravi
(Guest Lec. In-Charge)**

**Dr. M.Suman
(HOD-ECM)**