

## KONERU LAKSHMAIAH EDUCATION FOUNDATION

Green Fields, Vaddeswaram, Guntur Dist. - 522 502

M.Tech I Semester, In - Semester Examinations - II, February - 2021 (AY- 2020-2021) (Y20 Batch)

**TIME TABLE (January Admitted Batch)** 

M.Tech	10/03/2021	10/03/2021	11/03/2021	11/03/2021	12/03/2021	12/03/2021
ВТ	09.30 AM to 11.30 AM 19BT5101 Mathematics & Biostatistics	01.30 PM to 03.30 PM  19BT5102J Bio Chemical Engineering	09.30 AM to 11.30 AM 19BT5103J Molecular Biology RDNA Technology	01.30 PM to 03.30 PM  19BT5104J Applied Bio Informatics	09.30 AM to 11.30 AM  19BT51A1J Protein  Engineering	01.30 PM to 03.30 PM 19BT51B1J Food Technology
CE - SE	20CE5101J Advanced Mechanics of Solids	20CE5102J Advanced Prestressed Concrete Design	20CE5103J Advanced Concrete Technology	20CE5104J Structural Dynamics	20CE51A1J Pre Engineered Structures	20CE51B2J Repair and Rehabilitation of Structures
CE - CTM	20CE5121J Construction Planning Scheduling and Control	20CE5122J Sustainable Construction Materials and Methods	20CE5123J Lean Construction Practices	20CE5124J Building Information Modelling	20CE51E1J Material Procurement Management	20CE51F1J Construction Personnel Management
CE- GTE	20CE5161 Advanced Soil Mechanics	20CE5162 Sub Surface Investigations	20CE5163 Geo Environmental Engineering	20CE5164 Ground Improvement Techniques	20CE51M2 Finite Element Methods	20CE51N2 Design of Highways and Airfiled Pavements
CSE	18CS5101J Mathematical Foundations For Computer Science	18CS5102J Computer Organization & Architecture	18CS5103J Data Structures & Algorithms	18CS5104J Distributed Database Management Systems	18CS51A5J Machine Learning	18CS51B5J Software Verification & Validation
CSE- ML&C	18CS5109J Optimization Techniques	18CS5110J Applied Statistics	18CS5112J Matrix Computation	18CS5121J Data Warehousing & Mining	18CS51A5J Machine Learning	18CS51E4J Cloud Computing
ECE - VLSI	20EC5128J MOS Circuit Design	20EC5129J Analog IC Design & Design for Testability	20EC5130J ASIC & FPGA Design	20EC5131J IC Fabrication Technology	20EC51Q2J VLSI Signal Processing	20EC51R3J Semiconductor Device Modeling
ME - MD	18ME5117J Design Methods	18ME5118J Design with Advanced Materials	18ME5119J Theory of Elasticity and Plasticity	18ME5120J Modeling & Analysis 1 (CAD)	18ME51I2J Advanced Mechanisms	18ME51J1J Design of Pressure Vessels and Plates

## **Controller of Examinations**

Copy To: PA to VC Registrar Dean - SW Director - PG SO(E&E) Dean - Academics HOD & PG Coordinations of - BT CE CSE ECE EEE**ECSC** ME GH Library Helpdesk Transport BH