Outlook

A-6 Day Faculty Development Programme on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025 for Nominated Faculty of KLEF – Reg.

From Registrar Office <officeofregistrar@kluniversity.in>

Date Tue 2025-11-11 3:56 PM

To PRESIDENT <president@kluniversity.in>; Raja Harin koneru <krh@kluniversity.in>; PRO-CHANCELLOR <prochancellor@kluniversity.in>; Vice Chancellor - KLU <vc@kluniversity.in>; Dr.G.P.S. Varma, Vice- Chancellor <gpsvarma@kluniversity.in>; N Venkat Ram <venkatram@kluniversity.in>; Pro VC <provc@kluniversity.in>; ProVC-KRR <provc-krr@kluniversity.in>; Registrar <registrar@kluniversity.in>; Dr Jagadeesh Anne <drjagadeesh@kluniversity.in>; Joint Registrar <jointregistrar@kluniversity.in>; Deputy Registrar-II <deputyregistrar.2@kluniversity.in>; Prabhath Jasthi <prabhathjasthi@kluniversity.in>; A Krishna Rao <akr@kluniversity.in>; Assistant Registrar-Office of Registrar <asstregistrar@kluniversity.in>; Dr. A Vani <drvani@kluniversity.in>; Dr. JKR Sastry <drsastry@kluniversity.in>; Advisor Student Affairs <habibulla@kluniversity.in>; Dr. Subrahmanyam <smkodukula@kluniversity.in>; Dr Santosh Kumar <santosh@kluniversity.in>

3 attachments (7 MB)

Nominated Faculty List for Microchip FDP.xlsx; Microchip Embedded System Developer-01.jpg; Microchip Embedded System Developer-02.jpg;

Ref: KLEF/RO/Skill/2025-26 Date: 11-11-2025

Orders of the Hon'ble Vice-Chancellor dt.11-11-2025
CIRCULAR

Sub: A-6 Day Faculty Development Programme **on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025** for Nominated Faculty of KLEF – Reg.

Ref: Letter received from Dr. A. Srinath, Dean (Skill Development & Student Progression).

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This is to inform that the Skill Division and the Department of Electronics and Communication Engineering, in association with the Academic Staff College, EduSkill, and Microchip Technologies, are jointly organizing a 6-day Faculty Development Programme (FDP) on **"Microchip Embedded System Developer."** The details are as follows:

Resource Person : Mr. Prabhupada Samal, Assistant Manager
Learning & Development, EduSkills, Bhubaneswar
Dates : 17-11-2025 to 22-11-2025
Venue : R-604, R&D Block

The FDP aims to provide hands-on training in Microchip-based Embedded and VLSI System Design, with emphasis on 8-bit, 16-bit, and 32-bit Microcontrollers (MCUs) and advanced low-power technologies.

REGISTRAR

Encl: 1) Programme Brochure.
2) Nominated Faculty List.

Mail & Hard copy to: Hon'ble President, KLEF
Mail to: Hon'ble Vice-Presidents, KLEF
Mail & Hard copy to: Hon'ble Pro Chancellor
Mail & Hard copy to: Hon'ble Vice-Chancellor
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All Associate Deans / Deputy Deans / PR Head
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Mail to: Librarian
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Mail to: Chief Technical Officer (CTO)-Mr.A.Satya Kalyan
Mr. Raja Sekhar, Emp. No. 2482, Jr. Network Administrator (E-mail: rajasekhar_syte@kluniversity.in
Mail to: Professor In-charge, EduTech, Animation-Dr. M. Siva Kumar,Assoc. Professor,ECE
Mail to: Dean (Skill Development & Student Progression)
Principal-ASC
Individuals concerned

Thanks & Regards

O/o REGISTRAR
C-006, EXTN. NO. 1200
KONERU LAKSHMAIAH EDUCATION FOUNDATION
(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)
❖ Approved by AICTE ❖ ISO 21001:2018 Certified
Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.
Phone No. **08645 - 350200**; **www.klef.ac.in; www.klef.edu.in; www.kluniversity.in**

Organising Team. Chief Patron

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Smt. K. Siva Kanchana Latha, *Secretary, KLEF*
Er. K. L. Havish, *Vice President, KLEF*
Er. Raja Harin, *Vice President, KLEF*

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Dr. A.V.S. Prasad, *Pro Vice Chancellor, KLEF*
Dr. N. Venkataram, *Pro Vice Chancellor, KIFE*
Dr. K. Rajasekhar Rao, *Pro Vice Chancellor, KLEF*
Dr. K. Subba Rao, *Registrar, KLEF*
Dr. A. Jagadeesh, *CCO C Director FED, KLEF*
Dr. A. Srinath, *Dean Skill Development C Students Progression, KLEF*
Dr. Hari Kiran Vega. *Addl. Dean Academic, KLEF*
Dr. B. T. P. Madhav, *Dean R&D, KLEF*
Dr. K. Ramakrishna, *Dean IQAC, KLEF*
Dr. V. Srikanth, *Dean Faculty Affairs, KLEF*
Dr. K. R. S. Prasad, *Dean Student Affairs, KLEF*
Dr. N. B. V. Prasad, *Dean Placements, KLEF*
Dr. V. Rajesh, *Principal College of Engineering, KLEF*
Dr. V. Krishna Redddy, *Principal FED, KLEF*
Dr. Radhika Rani Chintala, *Principal Academic Staff College, KLEF*

Organizing Chairperson

Dr.I. Govardhani
Professor, HoD, Department of ECE

Convenor:

Dr. N Prabakaran
Associate Professor, ECE, KLEF
Dr. Aravindhan Alagarsamy,
Associate Professor, ECE, KLEF

Co- Convenor:

Dr. Arun Metha
Associate Professor, ECE.
Mr. M.Lakshmana Kumar,
Assistant Professor, ECE

Program Coordinator

Bibek Ranjan *Director*
+91-8093254904
bibek@eduskillsfoundation.org

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Electronics & Communication Engineering

Academic Staff College

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AICTE

MICROCHIP

Koneru Lakshmaiah Education Foundation
(Deemed to be University)

In Association with Eduskill and Microchip Technologies

A 6 days AICTE ATAL-Eduskills Faculty Development Program(FDP)

Microchip Embedded System Developer

17th to 22nd November 2025 I Venue: R Block 6th Floor



In person Mode

Organized by

Department of Electronics and Communication Engineering

In Association with

Skill Division & Academic Staff College

KLEF Deemed to be University, Green Fields, Vaddeswaram,
Guntur District, A.P., INDIA. Pincode : 522 302. Phone: 08645 - 350200.
https://www.kluniversity.in/

Report on

A-6 Day Faculty Development Programme on

"Microchip Embedded System Developer"

Dates: 17-11-2025 to 22-11-2025

Venue: R704B, Microchip Excel Centre, R-Block, KLEF.

Organizing Bodies:

Department of ECE, KLEF

Academic Staff College (ASC), KLEF

Skill Division, KLEF

AICTE-ATAL & EduSkills Foundation

Resource Persons:

Mr. Prabhupada Samal, Assistant Manager, Learning & Development, EduSkills, Bhubaneswar.

The Academic Staff College, KLEF in association with the Department of Electronics and Communication Engineering, Skill Division, AICTE-ATAL, and EduSkills Foundation, organized a six-day Faculty Development Programme on "**Microchip Embedded System Developer**" from 17th to 22nd November 2025. The programme was designed to provide faculty members with comprehensive knowledge and hands-on experience in embedded systems and VLSI design using Microchip technologies.

The FDP aimed to bridge the gap between theoretical understanding and industry-oriented practical skills. It focused on enabling participants to work with Microchip development tools and microcontrollers ranging from 8-bit to 32-bit architectures, along with exposure to low-power design techniques and real-time embedded applications.

Objectives of the Programme

- To enhance technical competencies in embedded systems and VLSI design
- To provide hands-on training using Microchip development platforms
- To strengthen programming and debugging skills in Embedded C
- To expose participants to real-time applications and industry practices
- To enable design of low-power and efficient embedded systems

Key Focus Areas

- Embedded Systems and Microcontroller Architecture
- Programming and Interfacing Techniques
- 8-bit, 16-bit, and 32-bit Microcontrollers
- Embedded C Programming and Debugging
- Low-Power Design Techniques
- Basics of VLSI System Design
- Real-Time Applications and Case Studies

Day 1: Introduction to Embedded Systems and Microchip Ecosystem

The first day of the FDP laid a strong conceptual foundation by introducing participants to the world of embedded systems. The session began with a detailed explanation of what embedded systems are, followed by their classification into real-time, standalone, networked, and mobile embedded systems. The resource person elaborated on how embedded systems are integral to modern technology, with applications spanning across domains such as consumer electronics, automotive systems, industrial automation, healthcare devices, and Internet of Things (IoT).

Participants were then introduced to the Microchip development ecosystem, which included tools such as MPLAB X IDE, XC series compilers, and hardware debugging tools like PICKit. The session provided a comprehensive walkthrough of the embedded system development lifecycle—from writing code and compiling it to debugging and deploying it on hardware.

Further, the architecture of microcontrollers was discussed in depth, including CPU operations, memory organization (Flash, SRAM, EEPROM), input/output ports, communication buses, and

clocking mechanisms. The session concluded with a demonstration of setting up the development environment, enabling participants to get ready for the upcoming hands-on sessions.

Day 2: Hands-on Training with 8-bit Microcontrollers

The second day focused extensively on 8-bit Microchip microcontrollers, particularly the PIC16 and PIC18 series. The resource person explained the architecture, instruction set, and register organization of these controllers, emphasizing their suitability for low-cost and resource-constrained applications.

A major highlight of the day was the hands-on training sessions, where participants actively worked on programming tasks using Embedded C in MPLAB X IDE with the XC8 compiler. The practical sessions were designed in a step-by-step manner to ensure clarity and ease of understanding.

Participants performed experiments such as LED blinking, pattern generation, push-button interfacing, and timer-based operations. They also worked on ADC-based sensor interfacing, where real-world data acquisition concepts were demonstrated. Additionally, UART communication was introduced, enabling participants to understand serial data transmission between devices.

The session also covered debugging techniques using PICKit programmers and simulators, helping participants identify and resolve errors effectively. By the end of the day, participants gained confidence in working with 8-bit microcontrollers and executing basic embedded system tasks independently.

Day 3: Advanced Applications using 16-bit Microcontrollers

On the third day, the focus shifted to 16-bit microcontrollers, specifically the dsPIC and PIC24 series, which offer enhanced computational capabilities and digital signal processing features. The resource person explained the architectural advancements, including pipeline execution, DSP engines, and multiply-accumulate (MAC) units.

Participants were introduced to advanced peripherals such as Pulse Width Modulation (PWM), Input Capture, and Output Compare modules. These concepts were reinforced through practical demonstrations and hands-on sessions.

The hands-on training included motor control applications using PWM signals, interfacing high-resolution analog sensors, and implementing interrupt-driven programming for real-time responsiveness. The session also introduced communication protocols such as SPI and I2C, with practical examples involving interfacing external devices like EEPROMs and sensors.

The day emphasized real-time system design and control applications, helping participants understand how embedded systems operate in dynamic environments. The combination of theory and practice enabled participants to develop a deeper understanding of intermediate-level embedded systems.

Day 4: High-Performance Computing with 32-bit Microcontrollers

The fourth day focused on 32-bit Microchip microcontrollers (PIC32 series), which are designed for high-performance and complex embedded applications. The session began with an overview of the MIPS-based architecture, highlighting features such as high-speed processing, large memory space, cache mechanisms, and Direct Memory Access (DMA).

Participants were introduced to advanced development tools using MPLAB X IDE with the XC32 compiler. The resource person explained how 32-bit controllers support sophisticated applications, including multitasking and real-time processing.

Hands-on sessions included GPIO control with high-speed execution, interfacing display modules such as LCD and OLED, and implementing communication protocols like UART and USB for data logging applications. An introduction to Real-Time Operating Systems (RTOS) was also provided, covering concepts such as task scheduling and multitasking.

The session included comparative analysis between 8-bit, 16-bit, and 32-bit controllers, helping participants understand performance trade-offs and application suitability. By the end of the day, participants were able to develop more complex embedded applications and appreciate the capabilities of high-end microcontrollers.

Day 5: Low-Power Design Techniques in Embedded Systems

The fifth day focused on one of the most critical aspects of modern embedded systems—power efficiency. The resource person emphasized the importance of low-power design in applications such as wearable devices, IoT systems, and battery-operated equipment.

Participants learned various techniques for reducing power consumption, including the use of sleep and idle modes, clock gating, and dynamic frequency scaling. The session also covered efficient coding practices that contribute to minimizing energy usage.

Hands-on demonstrations were conducted to show how low-power modes can be implemented in Microchip controllers. Participants analyzed power consumption patterns across different microcontroller families and understood how design choices impact energy efficiency.

Real-world case studies were discussed to illustrate how low-power design principles are applied in practical scenarios. This session provided valuable insights into designing sustainable and energy-efficient embedded systems.

Day 6: VLSI Integration, Applications, and Valedictory Session

The final day of the FDP focused on the integration of embedded systems with VLSI design concepts. The resource person introduced participants to fundamental VLSI concepts, including CMOS technology, scaling, and semiconductor fabrication basics.

The session explored the relationship between embedded systems and VLSI through System-on-Chip (SoC) design, highlighting how hardware and software components are integrated into a single platform. Participants also learned about different implementation approaches such as ASICs, FPGAs, and microcontroller-based systems.

Discussions on hardware-software co-design approaches provided insights into optimizing system performance and efficiency. Participants were encouraged to apply their learning to real-world problem-solving and innovative project development.

The programme concluded with participant presentations, feedback sessions, and knowledge sharing. Participants expressed their learning experiences and appreciated the hands-on approach of the FDP. The valedictory session marked the successful completion of the programme, followed by certificate distribution.

Outcomes of the Programme

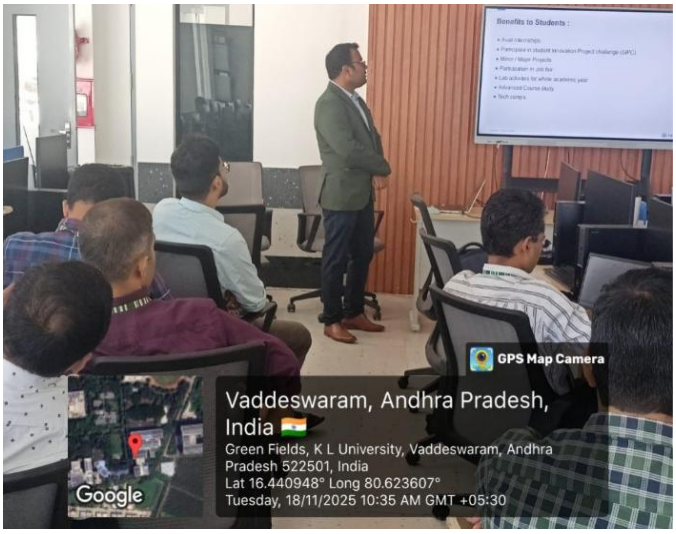
- Improved understanding of embedded system design using Microchip platforms
- Enhanced hands-on skills in programming and debugging
- Exposure to industry-standard tools and methodologies
- Ability to develop real-time and low-power embedded applications
- Strengthened knowledge in VLSI and system integration

Participants expressed high satisfaction with the programme, particularly appreciating the strong emphasis on hands-on learning. The practical sessions involving Microchip development boards, real-time experiments, and debugging techniques were found to be highly beneficial. The resource person was appreciated for delivering content in a clear and structured manner while providing continuous support during lab sessions.

The FDP successfully enabled participants to gain confidence in developing embedded applications independently. Many participants suggested organizing advanced-level programmes in embedded systems and VLSI design for further skill enhancement.

Photos:

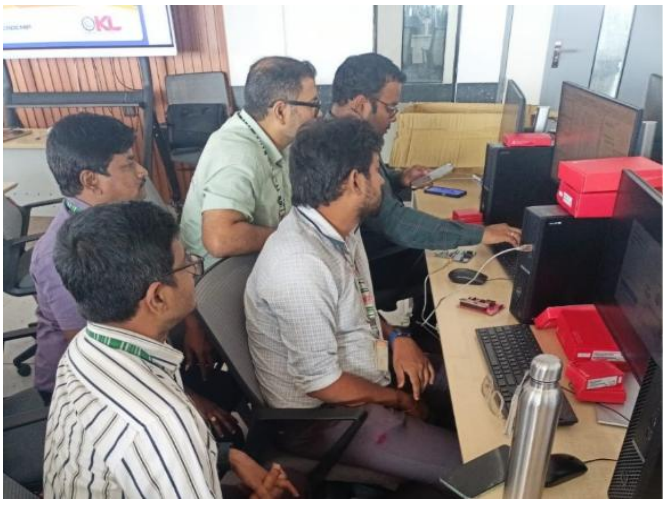



Benefits to Students :
GPS Map Camera
Vaddeswaram, Andhra Pradesh, India
Green Fields, K L University, Vaddeswaram, Andhra Pradesh 522501, India
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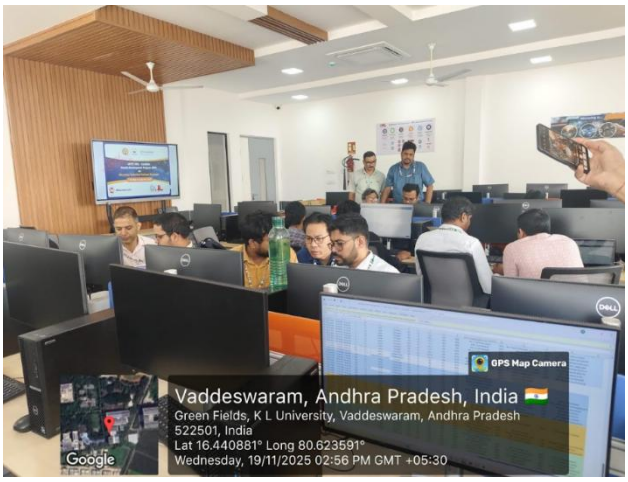

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KL University Main Road, Tadepalle, Guntur, Andhra Pradesh 522302, India
Lat 16.440665, Long 80.623377
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Note : Captured by GPS Map Camera

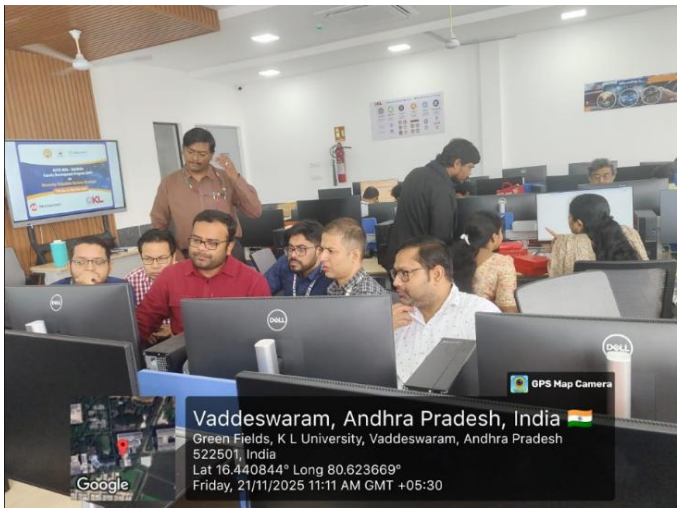

GPS Map Camera
Vaddeswaram, Andhra Pradesh, India
Green Fields, K L University, Vaddeswaram, Andhra Pradesh 522501, India
Lat 16.440861° Long 80.623658°
Thursday, 20/11/2025 11:40 AM GMT +05:30
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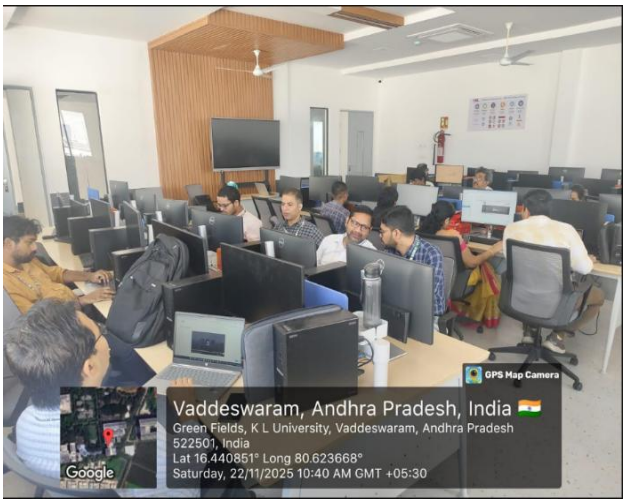


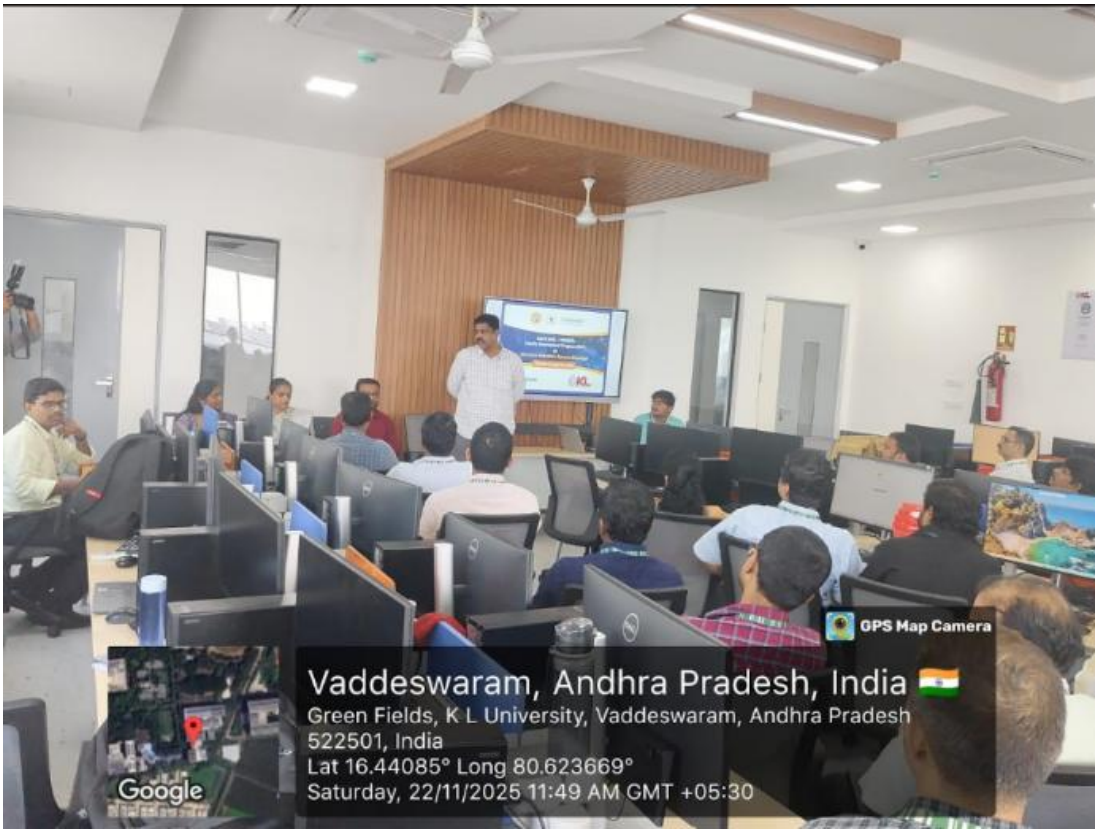



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Lat 16.440844° Long 80.623669°
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522501, India
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Conclusion

The six-day FDP was highly successful in delivering a comprehensive and practice-oriented learning experience. The integration of theoretical concepts with hands-on sessions significantly enhanced the technical capabilities of the participants.

The initiative taken by the Academic Staff College, Department of ECE, and collaborating organizations contributed effectively to strengthening faculty expertise in embedded systems and emerging technologies. Around 30 faculty from E12 and E29 cohorts have actively participated in the program.

Dr. Radhika Rani Chintala

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A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-1 :17-11-2025 .				Morning		Afternoon	
Sl No	Emp ID	Name of Faculty	Department	Slot-1	Slot-2	Slot-3	Slot-4
1	2980	Dr.V.Subba Reddy	ECE				
2	4295	Dr.A.V.Prabhu	ECE				
3	4822	Dr. K.V.Sowmya	ECE				
4	4966	Dr.N.Prabakaran	ECE				
5	5129	Dr.C.S. Preetham Reddy	ECE				
6	6224	Dr.Atul Kumar	ECE				
	9528	Dr Ashish Mishra	ECE				
8	9559	Dr Deblu Sahu	ECE				
9	8795	B.Suresh	CSE				
10	8806	MR. T Chithra kumar	CSE		Exam duty		
11	8913	Dr. G.Ravikanth	CSE				
12	8104	Dr.Satrughan Kumar	CSE				
13	4747	Dr. J Rajasekar	Skill Division				
14	5064	Dr.Ch Bhupati	IoT				
15	8626	Dr.S.Surekha	IoT				
16	2455	Dr. M Siva Kumar	ECE				
	2456	Dr. Syed Inthiyaz	ECE				
18	2457	Dr. Hari Kishore Kakarla	ECE				
19	2902	Dr. G R K Prasad	ECE				
20	4148	P.Srikanth Reddy	ECE				
21	4272	Dr.Sai Krishna Santosh	ECE				
22	4773	Dr K Girija Sravani	ECE				
23	5155	Dr.Bukya Balaji	ECE				
24	5239	M.Lakshmana Kumar	ECE				
25	5439	Dr. Chella Santhosh	ECE				
26	7158	Dr. Debajit Deb	ECE		Lab Exam	Examination	Exam
27	8417	Dr. Sandip Swarnakar	ECE		Lab exam		
28	9535	Dr. Yumnam Shantikumar Singh	ECE		Exam	1st hr class	
29	9539	Dr. Vivek Bhatt	ECE				
30	9680	Dr. Jitumani Sarma	ECE				

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Day-1: 17-11-2025.				Morning		Afternoon	
Sl.NO	EMP-ID	Name of the faculty	Department	Slot-1	Slot-2	Slot-3	Slot-4
1.	5616	Dr. S. Arunmozhi.	ECE ELSCOE	✓	✓	✓	✓
	5101	Dr. K.T.P.S. Kumar	I.T	✓	✓	✓	✓

A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-2 :18-11-2025 .

Sl No	Emp ID	Name of Faculty	Department	Morning		Afternoon	
				Slot-1	Slot-2	Slot-3	Slot-4
1	2980	Dr.V.Subba Reddy	ECE	Medical Leave			
2	4295	Dr.A.V.Prabhu	ECE	Lab Exam			
3	4822	Dr. K.V.Sowmya	ECE	LAB EXAM	LAB EXAM		
4	4966	Dr.N.Prabakaran	ECE				
5	5129	Dr.C.S. Preetham Reddy	ECE				
6	6224	Dr.Atul Kumar	ECE				
	9528	Dr Ashish Mishra	ECE				
8	9559	Dr Deblu Sahu	ECE				
9	8795	B.Suresh	CSE				
10	8806	MR. T Chithra kumar	CSE	Exam duty			
11	8913	Dr. G.Ravikanth	CSE	Exam duty			
12	8104	Dr.Satrughan Kumar	CSE	Exam duty	Exam		
13	4747	Dr. J Rajasekar	Skill Division				
14	5064	Dr.Ch Bhupati	IoT	ABSENT			
15	8626	Dr.S.Surekha	IoT	Exam	Exam		
16	2455	Dr. M Siva Kumar	ECE				
	2456	Dr. Syed Inthiyaz	ECE				
18	2457	~~Dr. Hari Kishore Kakarla~~	~~ECE~~				
19	2902	Dr. G R K Prasad	ECE	Absent			
20	4148	~~P.Srikanth Reddy~~	ECE				
21	4272	~~Dr.Sai Krishna Santosh~~	ECE	Absent			
22	4773	Dr K Girija Sravani	ECE	Lab Exam			
23	5155	Dr.Bukya Balaji	ECE	Lab Exam			
24	5239	M.Lakshmana Kumar	ECE				
25	5439	Dr. Chella Santhosh	ECE				
26	7158	Dr. Debajit Deb	ECE			Exam	Exam
27	8417	Dr. Sandip Swarnakar	ECE				
28	9535	Dr. Yumnam Shantikumar Singh	ECE				
29	9539	Dr. Vivek Bhatt	ECE				
30	9680	Dr. Jitumani Sarma	ECE				

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Koneru Lakshmaiah Education Foundation
(Deemed to be University)
Green Fields, VADDESWARAM-522 302

Day-2 :18-11-2025 .				Morning		Afternoon	
31	566	Dr. S. Arunmozhi.	ECE				
32	8212	P. Sivani	ECE				
33	5701	K T P S Kumar	IOT				
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A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-3: 19-11-2025 .				Morning		Afternoon	
Sl No	Emp ID	Name of Faculty	Department	Slot-1	Slot-2	Slot-3	Slot-4
1	2980	Dr.V.Subba Reddy	ECE				
2	4295	Dr.A.V.Prabhu	ECE				
3	4822	Dr. K.V.Sowmya	ECE				
4	4966	Dr.N.Prabakaran	ECE			Q my interview	Q my interview
5	5129	Dr.C.S. Preetham Reddy	ECE				
6	6224	Dr.Atul Kumar	ECE				
	9528	Dr Ashish Mishra	ECE		Class altered		
8	9559	Dr Deblu Sahu	ECE				
9	8795	B.Suresh	CSE				
10	8806	MR. T Chithra kumar	CSE				
11	8913	Dr. G.Ravikanth	CSE				
12	8104	Dr.Satrughan Kumar	CSE				
13	4747	Dr. J Rajasekar	Skill Division				
14	5064	Dr.Ch Bhupati	IoT			Theory Talks	Theory Talks
15	8626	Dr.S.Surekha	IoT	LAB EXAM			
16	2455	Dr. M Siva Kumar	ECE				
17	2456	Dr. Syed Inthiyaz	ECE				
~~18~~	~~2457~~	~~Dr. Hari Kishore Kakarla~~	~~ECE~~				
19	2902	Dr. G R K Prasad	ECE				
~~20~~	~~4148~~	~~P.Srikanth Reddy~~	~~ECE~~				
~~21~~	~~4272~~	~~Dr.Sai Krishna Santosh~~	~~ECE~~				
22	4773	Dr K Girija Sravani	ECE				
23	5155	Dr.Bukya Balaji	ECE				
24	5239	M.Lakshmana Kumar	ECE				
25	5439	Dr. Chella Santhosh	ECE				
26	7158	Dr. Debajit Deb	ECE				
27	8417	Dr. Sandip Swarnakar	ECE				
28	9535	Dr. Yumnam Shantikumar Singh	ECE	Lab exam			
29	9539	Dr. Vivek Bhatt	ECE				
30	9680	Dr. Jitumani Sarma	ECE				

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		Day-3: 19-11-2025 .		Morning		Afternoon	
31	5616	Dr. S Arun Meitei	LAB EXAM	← Amt			→
32	8212	P. SIVANI	Sivani	RS	RS	RS	RS
33	5101	K.T.P.S KUMAR	LEAVE		Amma		
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A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-4 : 20-11-2025 .				Morning		Afternoon	
Sl No	Emp ID	Name of Faculty	Department	Slot-1	Slot-2	Slot-3	Slot-4
1	2980	Dr.V.Subba Reddy	ECE				
2	4295	Dr.A.V.Prabhu	ECE				
3	4822	Dr. K.V.Sowmya	ECE	(leave)			
4	4966	Dr.N.Prabakaran	ECE				
5	5129	Dr.C.S. Preetham Reddy	ECE				
6	6224	Dr.Atul Kumar	ECE				
	9528	Dr Ashish Mishra	ECE				
8	9559	Dr Deblu Sahu	ECE				
9	8795	B.Suresh	CSE				
10	8806	Dr. T Chithrakumar	CSE				
11	8913	Dr. G.Ravikanth	CSE				
12	8104	Dr.Satrughan Kumar	CSE				
13	4747	Dr. J Rajasekar	Skill Division				
14	5064	Dr.Ch Bhupati	IoT				
15	8626	Dr.S.Surekha	IoT				
16	2455	Dr. M Siva Kumar	ECE				
	2456	Dr. Syed Inthiyaz	ECE				
1819	2902	Dr. G R K Prasad	ECE				
1922	4773	Dr K Girija Sravani	ECE				
2023	5155	Dr.Bukya Balaji	ECE				
2124	5239	M.Lakshmana Kumar	ECE				
2225	5439	Dr. Chella Santhosh	ECE				
2326	7158	Dr. Debajit Deb	ECE				
2427	8417	Dr. Sandip Swarnakar	ECE				
2528	9535	Dr. Yumnam Shantikumar Singh	ECE				
2629	9539	Dr. Vivek Bhatt	ECE				
2730	9680	Dr. Jitumani Sarma	ECE				
2831	5616	Dr.S.Arun mehtha	ECE				
2932	8212	Ms.P.Sivani	ECE				
3033	5101	Dr.T.P.S.KUMAR KUSUMANCHI	IoT				

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A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-5 : 20-11-2025 .				Morning		Afternoon	
Sl No	Emp ID	Name of Faculty	Department	Slot-1	Slot-2	Slot-3	Slot-4
1	2980	Dr.V.Subba Reddy	ECE				
2	4295	Dr.A.V.Prabhu	ECE				
3	4822	Dr. K.V.Sowmya	ECE				
4	4966	Dr.N.Prabakaran	ECE				
5	5129	Dr.C.S. Preetham Reddy	ECE			Exam duty	
6	6224	Dr.Atul Kumar	ECE			Exam duty	
	9528	Dr Ashish Mishra	ECE				
8	9559	Dr Deblu Sahu	ECE				
9	8795	B.Suresh	CSE			Exam duty	
10	8806	Dr. T Chithrakumar	CSE			Exam duty	
11	8913	Dr. G.Ravikanth	CSE			Exam duty	
12	8104	Dr.Satrughan Kumar	CSE				
13	4747	Dr. J Rajasekar	Skill Division				
14	5064	Dr.Ch Bhupati	IoT	ABSENT	ABSENT	ABSENT	ABSENT
15	8626	Dr.S.Surekha	IoT			Exam duty	
16	2455	Dr. M Siva Kumar	ECE				
	2456	Dr. Syed Inthiyaz	ECE				
18	2902	Dr. G R K Prasad	ECE				
19	4773	Dr K Girija Sravani	ECE				
20	5155	Dr.Bukya Balaji	ECE				
21	5239	M.Lakshmana Kumar	ECE				
22	5439	Dr. Chella Santhosh	ECE				
23	7158	Dr. Debajit Deb	ECE				
24	8417	Dr. Sandip Swarnakar	ECE			Meeting	
25	9535	Dr. Yumnam Shantikumar Singh	ECE			Meeting	
26	9539	Dr. Vivek Bhatt	ECE			Meeting	
27	9680	Dr. Jitumani Sarma	ECE			Meeting	
28	5616	Dr.S.Arun mehtha	ECE			inspection	
29	8212	Ms.P.Sivani	ECE			Class	
30	5101	Dr.T.P.S.KUMAR KUSUMAANCHI	IoT	ABSENT	ABSENT		

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A-6 Day Faculty Development Programme (FDP) on "Microchip Embedded System Developer" from 17-11-2025 to 22-11-2025.

Day-6 : 22-11-2025 .

Sl No	Emp ID	Name of Faculty	Department	Morning Slot-1	Morning Slot-2
1	2980	Dr.V.Subba Reddy	ECE		
2	4295	Dr.A.V.Prabhu	ECE		
3	4822	Dr. K.V.Sowmya	ECE		
4	4966	Dr.N.Prabakaran	ECE		
5	5129	Dr.C.S. Preetham Reddy	ECE		
6	6224	Dr.Atul Kumar	ECE		
	9528	Dr Ashish Mishra	ECE		
8	9559	Dr Deblu Sahu	ECE		
9	8795	B.Suresh	CSE		
10	8806	Dr. T Chithrakumar	CSE	Invigilation	
11	8913	Dr. G.Ravikanth	CSE		
12	8104	Dr.Satrughan Kumar	CSE	Invigilation	
13	4747	Dr. J Rajasekar	Skill Division		
14	5064	Dr.Ch Bhupati	IoT		
15	8626	Dr.S.Surekha	IoT		
16	2455	Dr. M Siva Kumar	ECE		
	2456	Dr. Syed Inthiyaz	ECE		
18	2902	Dr. G R K Prasad	ECE		
19	4773	Dr K Girija Sravani	ECE		
20	5155	Dr.Bukya Balaji	ECE		
21	5239	M.Lakshmana Kumar	ECE		
22	5439	Dr. Chella Santhosh	ECE		
23	7158	Dr. Debajit Deb	ECE		
24	8417	Dr. Sandip Swarnakar	ECE		
25	9535	Dr. Yumnam Shantikumar Singh	ECE		
26	9539	Dr. Vivek Bhatt	ECE		
27	9680	Dr. Jitumani Sarma	ECE		
28	5616	Dr.S.Arun mehtha	ECE		
29	8212	Ms.P.Sivani	ECE		
30	5101	Dr.T.P.S.KUMAR KUSUMANCHI	IoT		

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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Dave Paloian
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Dave Paloian
Academic Program Manager
Microchip

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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Academic Program Manager
Microchip

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Dave Paloian
Academic Program Manager
Microchip

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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Dave Paloian
Academic Program Manager
Microchip

Dr. Shubhajit Jagadev
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Dr. Sunil Luthra
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Dave Paloian
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Dave Paloian
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Dr. Arun Metha S

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K L University

for his excellent contribution as coordinator in

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of

K L University

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