
Hands-on training on “Testing and Verification of VLSI Circuits” by Mr Ch. Ramakrishna, Technical Manager at AMD, for Cohort E29 Faculty members, in association with Academic Staff College at KLEF – Communication – Reg.

From Registrar's Office <registraroffice@kluniversity.in>

Date Wed 2025-07-09 10:17 AM

To PRESIDENT <president@kluniversity.in>; Raja Harin koneru <krh@kluniversity.in>; PRO-CHANCELLOR <prochancellor@kluniversity.in>; Vice Chancellor - KLU <vc@kluniversity.in>; Dr.G.P.S. Varma, Vice- Chancellor <gpsvarma@kluniversity.in>; N Venkat Ram <venkatram@kluniversity.in>; Pro VC <provc@kluniversity.in>; Pro-VC-Academics <provc-academics@kluniversity.in>; Registrar <registrar@kluniversity.in>; Dr Jagadeesh Anne <drjagadeesh@kluniversity.in>; Joint Registrar <jointregistrar@kluniversity.in>; Deputy Registrar-II <deputyregistrar.2@kluniversity.in>; Prabhath Jasthi <prabhathjasthi@kluniversity.in>; A Krishna Rao <akr@kluniversity.in>; Assistant Registrar-Office of Registrar <asstregistrar@kluniversity.in>; Dr. A Vani <drvani@kluniversity.in>; Dr. JKR Sastry <drsastri@kluniversity.in>; Advisor Student Affairs <habibulla@kluniversity.in>; K R S Prasad <krsprasad_fed@kluniversity.in>; Dr. Subrahmanyam <smkodukula@kluniversity.in>

 1 attachment (70 KB)

E29 Cohort Members List.pdf;

Ref: KLEF/RO/Alumni Relations/2025-26

9th July 2025

Orders of the Hon'ble Vice-Chancellor dt. 09-07-2025
CIRCULAR

Sub: Hands-on training on “**Testing and Verification of VLSI Circuits**” by **Mr Ch. Ramakrishna, Technical Manager at AMD**, for Cohort E29 Faculty members, in association with Academic Staff College at KLEF – Communication – Reg.

Ref: Letter dated 08-07-2025 from Dr.K.Ch. Sri Kavya, Director (Alumni Relations) and forwarded by Dr. I. Govardhini, Principal, Academic Staff College.

This is to inform all Faculty members of KLEF that, as part of Professor of Practice Program, in association with Academic Staff College, **Mr CH. Ramakrishna, Technical Manager at AMD**, will be visiting KLEF to conduct Hands-on training for Faculty members of Cohort E29.

Cohort E29 Faculty are informed to attend the session.

Date : 10/07/25 & 11/07/25.

Venue : Sunflower Hall

Time : 10:00 AM to 12.00 Noon & 2.00 PM to 4.00 PM.

All **E29 Cohort faculty** are requested to attend the upcoming session of the **Professor of Practice Program**.

Heads of Department of ECE is requested to encourage faculty to participate and benefit from the program.

REGISTRAR

Encl: List of faculty to attend the program.

Mail to: Hon'ble President, KLEF

Mail to: Hon'ble Vice-Presidents, KLEF

Mail & Hard copy to: Hon'ble Pro Chancellor

Mail & Hard copy to: Hon'ble In-charge Vice-Chancellor

Mail & Hard copy to: Pro Vice-Chancellors

Mail to: Chief Coordinating Officer-Dr.A. Jagadeesh

Mail to: Special Officer -Dr.A. Vani

Mail to: All Advisors / All Deans / All Principals / All Vice-Principals

Mail to: All Sr.Directors / All Directors / All Additional Deans / All Associate Deans / Deputy Deans / PR Head

Mail to: Controller of Examinations-Dr.A.S.C.S.Sastry

Mail to: Joint Registrar / Deputy Registrar / Sr.Manager (Alumni Relations) & AR-Sri A.Krishna Rao / AR-Dr.MVAL Narasimha Rao

Mail to: All HoDs / All Alternate HoDs / All Dy. HoDs

Mail to: KL H - Principal Off Campus Centre, Aziz Nagar Campus / KL H - Principal Off Campus Centre, Bowrampet

Director (Academics) KLEF,GBS,Kondapur-Dr.G. Radha Krishna

Mail to: KL H HoDs of Off-campus Centres-Aziz Nagar / Bowrampet / GBS Kondapur

Mail to: KL H Associate Deans of Off-campus Centres-Aziz Nagar / Bowrampet / GBS Kondapur -

P&D / R&D / Academics / IQAC / F&SA / Skill Development / Student Progression & Training / Student Affairs / Student Welfare / Examinations

Mail to: Librarian

Mail to: Webmaster-Mr.LV

Mail to: Chief Technical Officer (CTO)-Mr.A.Satya Kalyan

Mr. Raja Sekhar, Emp. No. 2482, Jr. Network Administrator (E-mail: rajasekhar_syte@kluniversity.in)

Mail to: Professor In-charge, EduTech, Animation-Dr. M. Siva Kumar,Assoc. Professor,ECE

Mail to: GM-Purchases / Manager–Campus Ambience & Beautification-Mr.T Shakthi Swaroop

Mail to: Professor In-charge, Technical Maintenance / Liberal Arts Club-Dr.R Subhakara Raju

Mail to: Physical Edn. /Library / Gen.Maintenance / Transport / Construction / Central Stores / Girls' Hostel/

Boys' Hostel / Exam.Sec / Automation / SyTe / ET S&A Groyp / IQAC / VEC / CAES / ASAS / Intl Relations / Hobby Clubs / Help Desk / PIPS / Women's Forum / Elecl.Wing / Security /

Canteen

Mail to: Director-Alumni Relations

HoD-ECE

All Faculty members of Department of ECE

Thanks & Regards,



O/o REGISTRAR

C-006, EXTN. NO. 1200

KONERU LAKSHMAIAH EDUCATION FOUNDATION

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❖ Approved by AICTE ❖ ISO 21001:2018 Certified

Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.

Phone No. 08645 - 350200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in



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**45 YEARS OF
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LEADERSHIP**

RANKED 26
AMONG ALL
UNIVERSITIES

HANDS-ON TRAINING ON TESTING AND VERIFICATION OF VLSI CIRCUITS

FOR COHORT E29 FACULTY MEMBERS

10th - 11th July 2025

RESOURCE PERSONS

Mr CH. Ramakrishna
Technical Manager at AMD

Organised by Academic Staff College



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2024
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RANKING
FRAMEWORK

**RANKED 22
AMONG ALL
UNIVERSITIES**

Report on Hands-on Training on “Testing and Verification of VLSI Circuits” under Professor of Practice for Cohort-E29 Faculty

Organized by: Academic Staff College (ASC) in association with KL-Alumni Relations

Dates: 10th & 11th July 2025

Participants: Faculty Members of Cohort E29 (KL University)

Resource Person: Mr. Ch. Ramakrishna, Technical Manager, Advanced Micro Devices (AMD)

Venue: Sunflower Hall, C-Block and VLSI Lab, R-Block.

Day 1: 10-07-2025

Inauguration & Session 1: Fundamentals of VLSI Testing

Time: 10:00 AM – 12:00 PM

The program commenced with a formal inauguration by Dr. Aravindhan Alagarsamy, Professor incharge, VLSI Cohort (E29), who emphasized the importance of continuous faculty upskilling in VLSI, given its rapid advancements. He highlighted KL University’s commitment to fostering industry-academia collaboration through such initiatives.

The day-1 session started by Mr. Ch. Ramakrishna with an engaging presentation on “Why Testing and Verification are Critical in VLSI?”, drawing parallels between semiconductor manufacturing and quality assurance in automotive/aerospace industries.

Key Takeaways:

1. VLSI Testing vs. Verification:
 - Verification: Ensures the design meets specifications (pre-silicon).

- Testing: Detects manufacturing defects (post-silicon).
- Example: A bug in a processor's branch predictor (verification issue) vs. a short circuit due to lithography errors (testing issue).



2. Design for Testability (DFT) Techniques:

- Scan Chains: Inserting flip-flops to improve testability.
- Built-In Self-Test (BIST): On-chip test generation for memories.
- Automatic Test Pattern Generation (ATPG): Algorithms for fault detection.

3. Hands-on Activity:

- Participants used Synopsys TetraMAX to generate test patterns for a sample ISCAS-89 benchmark circuit.
- Demonstrated fault coverage analysis and discussed trade-offs between test time and coverage.

Session 2: Functional Verification with System Verilog & UVM

Time: 2:00 PM – 4:00 PM

The afternoon session focused on pre-silicon verification, a critical phase in chip design. Mr. Ramakrishna introduced Universal Verification Methodology (UVM), the backbone of modern verification environments.

Key Concepts Covered:

1. UVM Testbench Architecture:

- Transaction-level modeling (TLM) for high-level abstraction.
- Sequencers, Drivers, Monitors, and Scoreboards explained with a FIFO example.

2. System Verilog Assertions (SVA):

- Live demo of writing assertions for an AMBA AHB protocol.
- Importance of cover points in ensuring scenario completeness.

3. Hands-on Lab:

- Participants worked in pairs to develop a UVM testbench for a 4-bit adder.
- Debugged a race condition in the testbench, emphasizing synchronization.

Industry Insights:

- Mr. Ramakrishna shared how AMD uses formal verification alongside UVM for safety-critical designs.
- Faculty discussed challenges in teaching UVM—resource constraints, tool accessibility.

Day 2: 11th July 2025

Session 3: Physical Verification & Silicon Debug

Time: 10:00 AM – 12:00 PM

This session shifted focus to post-layout verification and real-world silicon debugging.

Key Topics:

1. Physical Verification Flow:
 - Design Rule Checking (DRC): Ensuring manufacturability.
 - Layout vs. Schematic (LVS): Preventing mismatches.
 - Electrical Rule Checking (ERC): Detecting floating nodes.
2. Case Study: Silicon Failure Analysis
 - Electromigration in power delivery networks.
 - IR Drop analysis using Cadence Voltus.
3. Hands-on Exercise:
 - Ran DRC/LVS checks on a 45nm inverter layout using Mentor Calibre.
 - Identified and resolved a metal spacing violation.

Industry Perspective:

- “A single DRC error can cost millions in respins,” Mr. Ramakrishna noted, stressing the need for rigorous checks.

Session 4: Emerging Trends

Time: 2:00 PM – 4:00 PM

The final session explored cutting-edge trends, followed by a panel discussion on academia-industry alignment.

Highlights:

1. AI/ML in VLSI Testing:
 - Predictive models for fault classification.
 - Reinforcement learning for test pattern optimization.
2. Automotive VLSI Challenges:
 - ISO 26262 Functional Safety requirements.
 - Redundancy techniques for ASIL-D compliance.
3. Open Discussion:
 - Faculty shared experiences in setting up VLSI labs.
 - Alumni relations team proposed industry mentorship programs.

The hands-on training was well received by the participants and the feedback was positive. Total 22 faculty from E29 cohort attended the program.

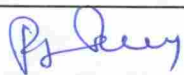
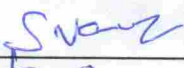
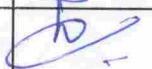
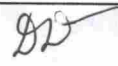
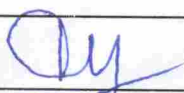
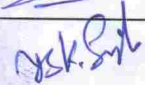


Dr. I. Govardhani,
Principal, Academic Staff College

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Hands-on training on “Testing and Verification of VLSI Circuits” by Mr Ch. Ramakrishna, Technical Manager at AMD, for Cohort E29 Faculty member From 10/07/25 & 11/07/25. Day -1,10-07-2025. Afternoon

Sl No	Emp ID	Name of Faculty	Department	InTime	Signature
1	2455	DrMSivaKumar	ECE	2:00	
2	2457	Dr.KHariKishore	ECE	2:15	
3	2484	Dr.B.kalivaraprasad(Transferred toFED)			
4	2488	Dr.FazalNoorbasha	ECE	2:15	
5	3867	Dr.K.SrinivasaRao			
6	4148	Mr.P.SrikanthReddy	ECE	2:15	
7	4272	Dr.G.SaiKrishnaSantosh			
8	4695	Dr.R.S.ErnestRavindran	ECE	2:00	
9	4773	DrKGirijaSravani	ECE	2:00	
10	5155	Dr.B.Balaji	ECE	2:00	
11	5239	Mr.MLakshmanaKumar	ECE	2:00	
12	5406	Dr.S.Rooban	ECE	2:00	
13	5439	Dr.ChellaSanthosh	ECE	2:00	
14	5477	Dr.SVamsheeKrishna	ECE	2:00	
15	6365	Dr.AravindhanaAlagarsamy	ECE	2:00	
16	7158	Dr.DebajitDeb	ECE	2:00	
17	7501	Dr.VivekananthanVenkateswaran			
18	8214	DrNagamalliArasavalli(Transferred toFED)	BES-II	2:05	
19	8417	Dr.SandipSwarnakar	ECE	2:10	
20	9535	Dr.Y.ShantikumarSingh	ECE	2:00	
21	9539	Dr.VivekBhatt	ECE	2:00	
22	9528	Dr.AshishKumar	ECE	2:00	

23. 2456 Dr. Syed Farhane
24 4021 Dr. M. Syatha
25 Dr. Gireesh Prasad
26 8381 B. Mamertha

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Sl No	Emp ID	Name of Faculty	Department	In Time	Signature
1	2455	DrMSivaKumar	E.C.E	10:00	M
2	2457	Dr.KHariKishore	ECE	10:30	V
3	2484	Dr.B.kalivaraprasad(Transferred to FFD)			
4	2488	Dr.FazalNoorbasha	ECE	18:15	D
5	3867	Dr.K.SrinivasaRao	ECE	10:00	Reddy
6	4148	Mr.P.SrikanthReddy	ECE	10:00	P. S. Reddy
7	4272	Dr.G.SaiKrishnaSantosh			
8	4695	Dr.R.S.ErnestRavindran			
9	4773	DrKGirijaSravani	ECE	10:00 AM	G
10	5155	Dr.B.Balaji	ECE	10:00	B
11	5239	Mr.MLakshmanaKumar	ECE	10:00	L
12	5406	Dr.S.Rooban	ECE	10:10	S. Rooban
13	5439	Dr.ChellaSanthosh	ECE	10:50	C. S.
14	5477	Dr.SVamsheeKrishna	ECE	10:10	S. Vamshee
15	6365	Dr.AravindhanaAlagarsamy	ECE	10:00	A. Aravindhana
16	7158	Dr.DebajitDeb	ECE	10:10	D. Debajit
17	7501	Dr.VivekananthanVenkateswaran			
18	8214	DrNagamalliArasavalli(Transferred to FFD)	FED-BES-II	10:05	N
19	8417	Dr.SandipSwarnakar	ECE	10:00	S. Sandip
20	9535	Dr.Y.ShantikumarSingh	ECE	10:00	Y. S.
21	9539	Dr.VivekBhatt	ECE	10:00	V. Bhatt
22	9528	Dr.AshishKumar	ECE	10:00	A. Ashish

23 2902 Dr. G. R. K. Prasad
24 2456 Dr. S. R. Rooban

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10-07-25
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Hands-on training on "Testing and Verification of VLSI Circuits" by Mr Ch. Ramakrishna, Technical Manager at AMD, for Cohort E29 Faculty member From 10/07/25 & 11/07/25.

Day -2,11-07-2025. Morning.

Sl No	Emp ID	Name of Faculty	Department	Out Time	Signature
1	2455	DrMSivaKumar	ECE	10:00	
2	2457	Dr.KHariKishore	ECE	10:30	
3	2484	Dr.B.kalivaraprasad	ECE	10:30	
4	2488	Dr.FazalNoorbasha	ECE	10:00	
5	3867	Dr.K.SrinivasaRao	ECE	10:00	
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17	7501	Dr.VivekananthanVenkateswaran	ECE	10:00	
18	8214	DrNagamalliArasavalli	ECE		
19	8417	Dr.SandipSwarnakar	ECE		
20	9535	Dr.Y.ShantikumarSingh			
21	9539	Dr.VivekBhatt			
22		Dr.AshishKumar			

23. 2902
24. 2456

Dr. GR K. Prasad
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Day -2,11-07-2025. Afternoon.

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9	4773	DrKGirijaSravani	ECE	2.00	
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13	5439	Dr.ChellaSanthosh	ECE		
14	5477	Dr.SVamsheeKrishna	ECE	2:00	
15	6365	Dr.AravindhanaAlagarsamy	BEE	2.00	
16	7158	Dr.DebajitDeb	BEE	2pm	
17	7501	Dr.VivekananthanVenkateswaran			
18	8214	DrNagamalliArasavalli			
19	8417	Dr.SandipSwarnakar	ECE	2:00	
20	9535	Dr.Y.ShantikumarSingh	ECE	2:00	
21	9539	Dr.VivekBhatt	ECE	2:00	
22		Dr.AshishKumar	ECE	2:00	

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Dr. G. R. S. PRINCIPAL
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VADESWARAM-522 307

ECE 2.00
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45 YEARS OF
EDUCATIONAL
LEADERSHIP
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INSTITUTIONAL
RANKING
FRAMEWORK 2024
RANKED 22
AMONG ALL
UNIVERSITIES

CERTIFICATE OF PARTICIPATION

This is to certify that

Dr.M.SivaKumar

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

Place: KLEF, Vaddeswaram.

Date : July 2025.

Dr. I. Govardhani
Principal
Academic Staff College



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This is to certify that

Dr. Fazal Noorbasha

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Mr.P.SrikanthReddy

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.R.S.ErnestRavindran

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.K.GirijaSravani

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.B.Balaji

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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Dr. I. Govardhani
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This is to certify that

Mr. MLakshmanaKumar

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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Dr. I. Govardhani
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45 YEARS OF
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FRAMEWORK 2024
RANKED 22
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UNIVERSITIES

CERTIFICATE OF PARTICIPATION

This is to certify that

Dr.S.Rooban

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

Place: KLEF, Vaddeswaram.

Date : July 2025.

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Dr.ChellaSanthosh

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.S.VamsheeKrishna

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Dr.AravindhanaAlagarsamy

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr. Debajit Deb

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.NagamalliArasavalli

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.SandipSwarnakar

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.Y.ShantikumarSingh

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This is to certify that

Dr. Vivek Bhatt

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.AshishKumar

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.SYED INTHIYAZ

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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This is to certify that

Dr.G.R.K.Prasad

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This is to certify that

Mrs.B.Mamatha

has Actively Participated in Hands-on training on **“Testing and Verification of VLSI Circuits”** for Cohort E29 Faculty members, of KLEF, Organized by the Alumni Relations KLEF, in association with Academic Staff College, KLEF, Vaddeswaram on 10/07/25 & 11/07/25.

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